

**EXERCISE: COMBINATIONAL CIRCUIT DESIGN:**

This exercise will take you through the full process of logic design from truth table to K-map minimization to implementation.

The objective of this exercise is to design a combinational circuit that is used to control a seven-segment display of decimal digits as shown in Figure 1.

The block diagram of the circuit is also given (Figure 2). The circuit has four inputs a, b, c, d and seven outputs A, B, C, D, E, F, G.

The input to the circuit is a Binary Coded Decimal (BCD) number, that is, abcd = 0000 represents decimal 0, 0001 represents decimal 1, 0010 is 2, 0011 is 3, 0100 is 4, 0101 is 5, 0110 is 6, 0111 is

7, 1000 is 8 and 1001 represents 9. The seven output lines, A, B, ..., G, each connected to one of the seven displays. Based on the input, the appropriate segments should "light up" showing the corresponding decimal number. For example, when the input is 0000, the outputs A, B, C, D, E and F are 1, while the output G is 0. Thus the output displays ZERO. (Figure 3).

The above reasoning shows you how to write the truth table. The first row is given below:

a	b	c	d	A	B	C	D	E	F	G
0	0	0	0	1	1	1	1	1	1	0

Figure 1: Seven Segment Display outputs

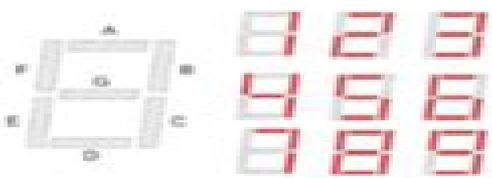


Figure 2: Block Diagram



Figure 3: Display of ZERO.

# Exercise 4 Combinational Circuit Design

**Guillaume Favre**



#### **Exercise 4 Combinational Circuit Design:**

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## **Table of Contents Exercise 4 Combinational Circuit Design**

1. Understanding the eBook Exercise 4 Combinational Circuit Design
  - The Rise of Digital Reading Exercise 4 Combinational Circuit Design
  - Advantages of eBooks Over Traditional Books
2. Identifying Exercise 4 Combinational Circuit Design
  - Exploring Different Genres
  - Considering Fiction vs. Non-Fiction
  - Determining Your Reading Goals
3. Choosing the Right eBook Platform
  - Popular eBook Platforms
  - Features to Look for in an Exercise 4 Combinational Circuit Design
  - User-Friendly Interface
4. Exploring eBook Recommendations from Exercise 4 Combinational Circuit Design
  - Personalized Recommendations
  - Exercise 4 Combinational Circuit Design User Reviews and Ratings

- Exercise 4 Combinational Circuit Design and Bestseller Lists
- 5. Accessing Exercise 4 Combinational Circuit Design Free and Paid eBooks
  - Exercise 4 Combinational Circuit Design Public Domain eBooks
  - Exercise 4 Combinational Circuit Design eBook Subscription Services
  - Exercise 4 Combinational Circuit Design Budget-Friendly Options
- 6. Navigating Exercise 4 Combinational Circuit Design eBook Formats
  - ePub, PDF, MOBI, and More
  - Exercise 4 Combinational Circuit Design Compatibility with Devices
  - Exercise 4 Combinational Circuit Design Enhanced eBook Features
- 7. Enhancing Your Reading Experience
  - Adjustable Fonts and Text Sizes of Exercise 4 Combinational Circuit Design
  - Highlighting and Note-Taking Exercise 4 Combinational Circuit Design
  - Interactive Elements Exercise 4 Combinational Circuit Design
- 8. Staying Engaged with Exercise 4 Combinational Circuit Design
  - Joining Online Reading Communities
  - Participating in Virtual Book Clubs
  - Following Authors and Publishers Exercise 4 Combinational Circuit Design
- 9. Balancing eBooks and Physical Books Exercise 4 Combinational Circuit Design
  - Benefits of a Digital Library
  - Creating a Diverse Reading Collection Exercise 4 Combinational Circuit Design
- 10. Overcoming Reading Challenges
  - Dealing with Digital Eye Strain
  - Minimizing Distractions
  - Managing Screen Time
- 11. Cultivating a Reading Routine Exercise 4 Combinational Circuit Design
  - Setting Reading Goals Exercise 4 Combinational Circuit Design
  - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Exercise 4 Combinational Circuit Design
  - Fact-Checking eBook Content of Exercise 4 Combinational Circuit Design
  - Distinguishing Credible Sources

13. Promoting Lifelong Learning
  - Utilizing eBooks for Skill Development
  - Exploring Educational eBooks
14. Embracing eBook Trends
  - Integration of Multimedia Elements
  - Interactive and Gamified eBooks

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