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APPLICATION NOTE 5504

Designing Flyback Converters Using Peak-Current-Mode Controllers

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Nov 27, 2012

Abstract: Flyback converter design using MAX17595/MAX17596 is outlined. Design methodology and calculations for components value selection are presented. Continuous conduction mode (CCM) and discontinuous conduction mode (DCM) are treated individually.

Introduction

This application note describes the methodology of designing flyback converters using the [MAX17595/MAX17596](#) peak-current-mode controllers. Flyback converters may be operated in discontinuous conduction mode (DCM) or continuous conduction mode (CCM). The component choices, stress level in power devices, and controller design vary depending on the operating mode of the converter. Formulas for calculating component values and ratings are also presented.

DCM Flyback

Primary Inductance Selection

In a DCM flyback converter, the energy stored in the primary inductance of the flyback transformer is delivered entirely to the output. The maximum primary-inductance value for which the converter remains in DCM at all operating conditions can be calculated as:

$$L_{PRIMAX} \leq \frac{(V_{RMIN} \times D_{MAX})^2 \times 0.4}{(V_{OUT} + V_D) \times I_{OUT} \times F_{SW}} \quad (\text{Eq. 1})$$

Where D_{MAX} is chosen as 0.43 for the MAX17595/MAX17596, V_D is the voltage drop of the output rectifier diode on the secondary winding, and F_{SW} is the switching frequency of the power converter. Choose the primary inductance value to be less than L_{PRIMAX} .

Duty Cycle Calculation

The accurate value of the duty cycle (D_{NEW}) for the selected primary inductance (L_{PR2}) can be

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Pulse Width Modulated DC-DC Converters Keng Chih Wu,2012-12-06 For the first time in power electronics this comprehensive treatment of switch mode DC DC converter designs addresses many analytical closed form equations such as duty cycle prediction output regulation output ripple control loop gain and steady state time domain waveform Each of these equations are given various topologies and configurations including forward flyback and boost converters Pulse Width Modulated DC DC Converters begins with a detailed approach to the quiescent operating locus of a power plant under open loop The reader is then led through other supporting circuits once again in the quiescent condition These exercises result in the close loop formulations of the subject system providing designers with the ability to study the sensitivities of a system against disturbances With the quiescent conditions well established the book then guides the reader further into the territories of system stability where small signal behaviors are explored Finally some important large signal time domain studies cap the treatment Some distinctive features of this book include detailed coverage of dynamic close loop converter simulations using only personal computer and modern mathematical software Steady state time domain analysis based on the concept of continuity of states Voltage mode and current mode control techniques and their differences of merits A detailed description on setting up different equations for DC DC converters simulation using only PC

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