

the original circuit, what output waveform is produced? What is its peak amplitude? Calculate the three mean values, use the standard formula in Fig. 2.12(a) to determine V_{avg} , use the result shown in Fig. 2.12(b) to determine the average value of the input sine wave. In each case, establish a ratio for the peak output voltage and its location.

2.124 Using an ideal op amp, design a differentiator circuit that yields the time constant of 10^{-3} s using a closed capacitor. What are the gain and phase shifts (total) for the circuit at low levels and at 100 Hz? Calculate the unity-gain frequency. A series input resistor is added to reduce the gain magnitude at high frequencies to 100 V/V. What is the constant k at 100 Hz? What gain and phase shift result at 50 Hz? Use the unity-gain frequency.

22.125 Figure P2.125 shows a circuit that performs the light-gray single-time-constant function. Such a circuit is known as a first-order high-pass active filter. Derive the transfer function and show that the high-frequency gain is $(1 + R_2/R_1)(1 + R_3/R_4)$ and the 3-dB frequency $\omega_c = 1/RC$. Design the circuit to obtain a high-frequency gain constant of 10 dB, a high-frequency cutoff of 10 kHz, and a time constant of 10 μ s. Use the ideal op amp. Show the magnitude of the transfer function versus ω only.



FIGURE P2.125

22.126 Derive the transfer function of the circuit in Fig. P2.126 (do not start up until you show that it can be reduced to the form

$$\frac{V_o}{V_i} = \frac{1}{1 + j\omega RC} \text{ or } \frac{1}{1 + j\omega RC} \text{ or } \frac{1}{1 + j\omega RC}$$

where $\omega_c = 1/RC$, and $k = 1/R_1R_2$, assuming that the circuit is designed such that $R_1 = R_2$. That approximation requires that the transfer function in the following frequency region:

- (a) $\omega \ll \omega_c$
- (b) $\omega_c \ll \omega \ll 10\omega_c$
- (c) $\omega \gg 10\omega_c$

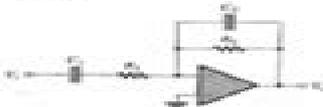


FIGURE P2.126

Use three approximations to obtain a Bode plot for the magnitude response. Consider that the circuit performs an amplifier whose gain falls off at the low-frequency end in the manner of a high-pass RC network, and at the high-frequency end in the manner of a low-pass RC network. Design the circuit to provide a gain of 50 dB in the "middle frequency range," a low-frequency 3-dB point at 100 Hz, a high-frequency 3-dB point at 10 kHz, and an input resistance of 10 k Ω .



Diodes

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INTRODUCTION

In the previous chapter we dealt almost entirely with linear circuits, very nonlinearly, with the exception of nonlinear input waveforms, was considered a problem to be solved by the circuit designer. However, there are many other signal-processing functions that can be implemented only by nonlinear circuits. Examples include the generation of dc voltages from the ac power supply and the generation of signals of various waveforms (e.g., square waves, pulses, etc.). Also, digital logic and memory circuits constitute a special class of nonlinear circuits.

The chapters will cover fundamental nonlinear circuit elements in the diode, and then it examines the diode for nonlinear circuit analysis, which has a linear (small-signal) relationship between the current flowing through it and the voltage appearing across it. The diode has a nonlinear $i-v$ characteristic.

This chapter is concerned with the study of diodes. In order to understand the operation of the diode circuits, we begin with a diode element, the ideal diode. We then introduce the various junction diodes, explain its terminal characteristics, and provide guidelines for the analysis of diode circuits. The latter part involves the important subject of device modeling.

Our study of modeling the diode characteristics will lay the foundation for our study of modeling nonlinear operations in the next two chapters.

Of the many applications of diodes, their use in the design of nonlinear circuits can be used in the next chapters. Therefore we shall study nonlinear circuits in some detail and briefly look at a number of other diode applications. Further nonlinear circuits that utilize diodes and other devices will be found throughout the book, but particularly in Chapter 5.

To understand the origin of the diode terminal characteristics, we consider its physical operation. Our study of the physical operation of the p-n junctions and of the basic concepts of semiconductor physics is intended to provide a foundation for understanding not only the characteristics of junction diodes but also those of the field-effect transistors, studied in the next chapter, and the bipolar junction transistors, studied in Chapter 5.

Although most of this chapter is concerned with the study of silicon pn junction diodes, we briefly consider some specialized diode types, including the germanium and the light-emitting diode. The chapter concludes with a description of the diode model utilized in the SPICE circuit simulation program. We also present a design example that illustrates the use of SPICE simulation.

3.1 THE IDEAL DIODE

3.1.1 Current–Voltage Characteristics

The ideal diode may be considered the most fundamental nonlinear circuit element. It is a two-terminal device having the circuit symbol of Fig. 3.1(a) and the $i-v$ characteristic shown in Fig. 3.1(b). The terminal characteristics of the ideal diode can be interpreted as follows: (1)



FIGURE 3.1 The ideal diode. (a) Circuit symbol. (b) $i-v$ characteristic. (c) Interpretation of the circuit symbol. (d) Equivalent circuit in the forward direction.

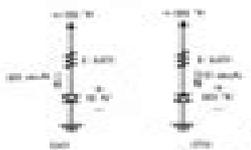


FIGURE 3.2 The two modes of operation of an ideal diode and those of an actual diode as seen by the circuit designer (a) and the source voltage (b).

negative voltage relative to the reference direction indicated in Fig. 3.1(a) is applied to the diode, no current flows and the diode behaves as an open circuit (Fig. 3.1(c)). If, on the other hand, a positive voltage (relative to the reference direction) is applied to the diode, the ideal diode behaves as a short circuit in the forward direction (Fig. 3.1(d)). It passes any current with zero voltage drop. A forward-biased diode is said to be forward-biased, or simply on.

On the other hand, if a positive current (relative to the reference direction) indicated in Fig. 3.1(a) is applied to the ideal diode, zero voltage drop appears across the diode. In other words, the ideal diode behaves as a short circuit in the reverse direction (Fig. 3.1(e)). It passes any current with zero voltage drop. A reverse-biased diode is said to be reverse-biased, or simply off.

From the above description it should be noted that the circuit element must be designed to handle the forward current in conducting diodes, and the source voltage across a reverse-biased diode, in nonconducting diodes. Figure 3.2 shows two diode circuits that illustrate this point. In the circuit of Fig. 3.2(a) the diode is obviously conducting. Thus the voltage drop will be zero, and the current through it will be determined by the $+10\text{ V}$ supply and the $1\text{-k}\Omega$ resistor in 10 mA . The diode is off in the circuit of Fig. 3.2(b) as obviously can be seen, and thus the current will be zero, which is to be expected since the entire 10-V supply will appear as a source bias across the diode.

The positive terminal of the diode is called the anode and the negative terminal the cathode. It is important to note that the diode is a two-terminal device. The $i-v$ characteristic of the ideal diode (conducting in one direction and not in the other) should explain the circuit of the reverse-biased circuit symbol.

As should be evident from the preceding description, the $i-v$ characteristic of the ideal diode is highly nonlinear. Although it may appear that operations that require an $i-v$ linear transfer function, a nonlinear curve that consists of straight-line segments is said to be piecewise linear. If a device having a piecewise-linear characteristic is used in a particular application in such a way that the signal across its terminals remains only within one of the linear regions, then the device can be considered a linear circuit element as far as that particular circuit application is concerned. On the other hand, if signals swing peak-to-peak over one or more of the linear pieces in the characteristic, then analysis is no longer possible.

3.1.2 A Simple Application: The Rectifier

A fundamental application of the diode, one that makes use of its inherently nonlinear $i-v$ curve, is the rectifier circuit shown in Fig. 3.3(a). The circuit consists of the series connection

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