



Fig 1

Design And Verification Of Axi Ocp Bridge Supporting Out

**Diana Goehringer, Marco Domenico
Santambrogio, João M.P. Cardoso, Koen
Bertels**

Design And Verification Of Axi Ocp Bridge Supporting Out:

Proceedings of the 5th International Conference on Frontiers in Intelligent Computing: Theory and Applications

Suresh Chandra Satapathy, Vikrant Bhateja, Siba K. Udgata, Prasant Kumar Pattnaik, 2017-03-02 The book is a collection of high quality peer reviewed research papers presented at International Conference on Frontiers of Intelligent Computing Theory and applications FICTA 2016 held at School of Computer Engineering KIIT University Bhubaneswar India during 16 17 September 2016 The book aims to present theories methodologies new ideas experiences applications in all areas of intelligent computing and its applications to various engineering disciplines like computer science electronics electrical mechanical engineering etc Reconfigurable Computing: Architectures, Tools, and Applications Diana

Goehringer, Marco Domenico Santambrogio, João M.P. Cardoso, Koen Bertels, 2014-04-09 This book constitutes the thoroughly refereed conference proceedings of the 10th International Symposium on Reconfigurable Computing Architectures Tools and Applications ARC 2014 held in Vilamoura Portugal in April 2014 The 16 revised full papers presented together with 17 short papers and 6 special session papers were carefully reviewed and selected from 57 submissions The topics covered are applications methods frameworks and OS for debug over clocking and relocation memory architectures methodologies and tools and architectures

Advances in Communication, Devices and Networking Rabindranath Bera, Subir Kumar Sarkar, Om Prakash Singh, Hemanta Saikia, 2019-02-15 The book covers recent trends in the field of devices wireless communication and networking It presents the outcomes of the International Conference in Communication Devices and Networking ICCDN 2018 which was organized by the Department of Electronics and Communication Engineering Sikkim Manipal Institute of Technology Sikkim India on 2 3 June 2018 Gathering cutting edge research papers prepared by researchers engineers and industry professionals it will help young and experienced scientists and developers alike to explore new perspectives and offer them inspirations on addressing real world problems in the field of electronics communication devices and networking

On-Chip Communication Architectures Sudeep Pasricha, Nikil Dutt, 2010-07-28 Over the past decade system on chip SoC designs have evolved to address the ever increasing complexity of applications fueled by the era of digital convergence Improvements in process technology have effectively shrunk board level components so they can be integrated on a single chip New on chip communication architectures have been designed to support all inter component communication in a SoC design These communication architecture fabrics have a critical impact on the power consumption performance cost and design cycle time of modern SoC designs As application complexity strains the communication backbone of SoC designs academic and industrial R D efforts and dollars are increasingly focused on communication architecture design On Chip Communication Architectures is a comprehensive reference on concepts research and trends in on chip communication architecture design It will provide readers with a comprehensive survey not available elsewhere of all current standards for on chip communication architectures A definitive guide to on chip communication

architectures explaining key concepts surveying research efforts and predicting future trends Detailed analysis of all popular standards for on chip communication architectures Comprehensive survey of all research on communication architectures covering a wide range of topics relevant to this area spanning the past several years and up to date with the most current research efforts Future trends that will have a significant impact on research and design of communication architectures over the next several years Design and Verification of Bus Bridge from Ocp to Ahb Ranganathan Sundaram,2012

Protocols are commonly used to connect IP blocks on structured SoCs Generally Protocol is the back bone of the SoC and its failure usually leads to a non functional chip In present market various types of standard protocols are available and are used in SoC which requires a bridge to pass the information from one type of protocol to other type of protocol safely and without any data loss OCP and AMBA AHB protocol are commonly used protocols In this work the bus bridge has been designed to interface these protocols which plays a vital role in SoC application such as it may lead to application failure if it doesn't work properly Initially OCP and AHB protocols are modeled separately using VHDL and are simulated Basically Bus Bridge should convert command and data of OCP formats to acceptable AHB formats This conversion does not ensure proper communication unless the timings of each protocol were met Hence the interconnecting Bus Bridge wrapper between Core Centric Protocol OCP and Bus Centric Protocol AHB was designed with proper timing delay Also the PSL assertions are written in order to verify the behavior of protocols Design Verification and Performance Analysis of Serial AXI Links in Broadcom System-on-Chip Simran Kaur Sarai,2014 Design verification is an essential step in the development of any product Also referred to as qualification testing design verification ensures that the product as designed is the same as the product as intended In this project design verification and performance analysis of Thin Advanced Extensible Interface Links T AXI is conducted on a Broadcom's SoC System on Chip T AXI is a Broadcom's proprietary bus that interfaces all the subsystems on the System onchip SoC to the system memory Test cases are developed to verify the functionality of the T AXI and performance verification is implemented using scenarios derived from real world examples A Field Programmable Gate Array FPGA is used to emulate the SoC design and C programming is used to write the test cases The test results verify the T AXI functionality and the performance analysis supports the theoretical calculations

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