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The International Symposium on High-Performance Computer Architecture provides a high-quality forum for scientists and engineers to present their latest research findings in this rapidly-changing field. Authors are invited to submit papers on all aspects of high-performance computer architecture. Topics of interest include, but are not limited to:

- Processor, cache, and memory architectures
- Parallel computer architectures
- Multicore architectures
- Impact of technology on architecture
- Power-efficient architectures and techniques
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- Embedded and reconfigurable architectures
- Interconnect and network interface architectures
- Architectures for cloud-based HPC and data centers
- Innovative hardware/software trade-offs
- Impact of compilers and system software on architecture
- Performance modelling and evaluation
- Architectures for emerging technology and applications

Authors should submit an abstract by Tuesday, September 3, 2013, 11:59 PM EDT. They should submit the full version of the paper by **Friday, September 6, 2013, 11:59 PM EDT**. **No extensions will be granted**. The full version should be a PDF file that does not exceed 11 pages (plus unlimited pages for references) according to the instructions in http://hpca20.ece.ufl.edu/. Papers that exceed the length limit or that cannot be viewed using Adobe Reader (version 3.0 or higher) may not be reviewed. Papers should be submitted for double-blind review. We anticipate making a Best Paper award; all papers will be evaluated based on their novelty, fundamental insights, and potential for long-term impact. New-idea papers are encouraged. Submission issues should be directed to the program chair at <enright@eecg.toronto.edu>. Workshop and tutorial submissions should be directed to the workshop and tutorial co-chairs (msatish@umich.edu, jmwu@ustc.edu.cn). HPCA-20 will host an **Industrial Paper Session** presenting novel insights from industry (see Call for Industry Papers at http://hpca20.ece.ufl.edu/).

Accepted papers will be published in the conference proceedings distributed to conference attendees. Papers will also be uploaded to IEEE Xplore.

Important dates
- Abstract deadline: **September 3, 2013, 11:59 PM EDT**
- Paper deadline: **September 6, 2013, 11:59 PM EDT**
- Workshop and tutorial proposals due: October 7, 2013
- Rebuttal period: **October 28-31, 2013**
- Notification of paper outcome: **November 15, 2013**

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    Post-Quantum Cryptography Thomas Johansson,Daniel Smith-Tone,2023-08-09 This book constitutes the refereed proceedings of the 14th International Workshop on Post Quantum Cryptography PQCrypto 2022 held in College Park MD USA in August 14 18 2023 The 25 full papers presented in this book were carefully reviewed and selected from 51 submissions They are categorized in the following topical sections code based cryptography group action based cryptography isogenye based cryptography lattice based cryptography multivariate cryptography quantum algorithms cryptanalysis and models post quantum protocols side channel cryptanalysis and countermeasures **Information Security and Cryptology** Xiaofeng Chen,Dongdai Lin,Moti Yung,2018-02-12 This book constitutes the thoroughly refereed post conference proceedings of the 13th International Conference on Information Security and Cryptology Inscrypt 2017 held in Xi an China in November 2017 The 27 revised full papers presented together with 5 keynote speeches were carefully reviewed and

selected from 80 submissions The papers are organized in the following topical sections cryptographic protocols and algorithms digital signatures encryption cryptanalysis and attack and applications **The Data Center as a Computer** Luiz André Barroso,Urs Hölzle,Parthasarathy Ranganathan,2025-12-27 Warehouse scale computers WSCs power cloud computing and all the great web services we use daily including recent advancements in Artificial Intelligence Machine Learning AI ML This book examines how WSCs treat the datacenter itself as one massive computer designed at warehouse scale with hardware and software working in concert The book details the architecture of WSCs and covers the main factors influencing their design operation and cost structure as well as the characteristics of their software base Each chapter contains real world examples including detailed case studies and previously unpublished details of the infrastructure used to power Google s online services Though targeted at the architects and programmers of today s WSCs this book will also be broadly interesting to those who just want to understand the infrastructure powering cloud computing and hyperscale and AI services The fourth edition reflects six years of advancements since the previous edition and includes significantly revamped and enhanced content including four new chapters new case studies and completely redone illustrations optimized for online reading New topics include exascale AI processing software defined infrastructure advanced networking modular datacenter design chip packaging and interconnects silent data corruption trusted computing sustainability and using AI to design better systems Also new to this edition the book includes a retrospective discussing key innovations and learnings from 25 years of warehouse scale computing and an annotated bibliography of our selection of top 25 papers from this period Updated discussions of emerging trends and opportunities ensure that this revised edition will remain an essential resource for educators and professionals working on the next generation of WSCs This is an open access book *Science of Cyber Security* Feng Liu,Jia Xu,Shouhuai Xu,Moti Yung,2019-12-06 This book constitutes the proceedings of the Second International Conference on Science of Cyber Security SciSec 2019 held in Nanjing China in August 2019 The 20 full papers and 8 short papers presented in this volume were carefully reviewed and selected from 62 submissions These papers cover the following subjects Artificial Intelligence for Cybersecurity Machine Learning for Cybersecurity and Mechanisms for Solving Actual Cybersecurity Problems e g Blockchain Attack and Defense Encryptions with Cybersecurity Applications

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