

| TRANSFER | | | |
|----------|------------------|-----------------|---------------------|
| Name | Comment | Code | Operation |
| MOV | Move (copy) | MOV Dest,Source | Dest=Source |
| XCHG | Exchange | XCHG Op1,Op2 | Op1:=Op2 , Op2:=Op1 |
| STC | Set Carry | STC | CF:=1 |
| CLC | Clear Carry | CLC | CF:=0 |
| CMC | Complement Carry | CMC | CF:= \neg CF |
| STD | Set Direction | STD | DF:=1 (string op's) |
| CLD | Clear Direction | CLD | DF:=0 (string op's) |
| STI | Set Interrupt | STI | IF:=1 |
| CLI | Clear Interrupt | CLI | IF:=0 |

Intel Assembly Language Manual

David C. Willen, Jeffrey I. Krantz



Intel Assembly Language Manual:

Asm86 Assembly Language Reference Manual/122386 Intel Corporation,1985 **ASM86 Language Reference Manual** Intel Corporation,Intel Corporation Staff,1983-01-01 **SIMD Programming Manual for Linux and Windows** Paul Cockshott,Kenneth Renfrew,2013-03-09 A number of widely used contemporary processors have instruction set extensions for improved performance in multi media applications The aim is to allow operations to proceed on multiple pixels each clock cycle Such instruction sets have been incorporated both in specialist DSPchips such as the Texas C62xx Texas Instruments 1998 and in general purpose CPU chips like the Intel IA32 Intel 2000 or the AMD K6 Advanced Micro Devices 1999 These instruction set extensions are typically based on the Single Instruction stream Multiple Data stream SIMD model in which a single instruction causes the same mathematical operation to be carried out on several operands or pairs of operands at the same time The level of parallelism supported ranges from two floating point operations at a time on the AMD K6 architecture to 16 byte operations at a time on the Intel P4 architecture Whereas processor architectures are moving towards greater levels of parallelism the most widely used programming languages such as C Java and Delphi are structured around a model of computation in which operations takeplace on a single value at a time This was appropriate when processors worked this way but has become an impediment to programmers seeking to make use of the performance offered by multi media instruction sets The introduction of SIMD instruction sets Peleg et al **Intel 8080 Assembly Language Programming Manual** Intel Corporation,1976 *Microprocessors and Microcomputer-Based System Design* Mohamed Rafiquzzaman,2021-11-01 *Microprocessors and Microcomputer Based System Design* Second Edition builds on the concepts of the first edition It discusses the basics of microprocessors various 32 bit microprocessors the 8085 microprocessor the fundamentals of peripheral interfacing and Intel and Motorola microprocessors This edition includes new topics such as floating point arithmetic Program Array Logic and flash memories It covers the popular Intel 80486 80960 and Motorola 68040 as well as the Pentium and PowerPC microprocessors The final chapter presents system design concepts applying the design principles covered in previous chapters to sample problems **Computing Center Memo** University of Michigan Computing Center,1979 *Asm286 Assembly Language Reference Manual* Intel Corporation,Intel Corporation Staff,1983-12 **Catalog of Copyright Entries. Third Series** Library of Congress. Copyright Office,1976 **Intel 8080 Assembly Language Programming Manual** Intel Corporation, *CP/M Assembly Language Programming* Ken Barbier,1983 **Introduction to Assembly Language Programming** Joerg Mayer,1988-08-16 Provides comprehensive coverage of all 8086 8088 and 8087 instructions assembler directives and the most important MS DOS and ROM BIOS functions Progressing from simple to complex tasks this text allows students to write complete programs prepare them for execution run them and use most of the facilities of the whole computer system Most sample programs are preceded by PASCAL and BASIC programs meeting the same specifications Includes detailed discussions and examples of CP M and

XENIX style file handling thorough coverage of graphics plus a thorough introduction to the 8087 coprocessor Also included are 180 exercises annotated tables of 8086 and 8087 instructions chapter summaries and lists of key words and numerous line drawings All 60 programs are accompanied by diskettes eliminating the need for lengthy typing **8088/IBM PC Assembly Language Programming** Gary A. Shade,1985 2920 Assembly Language Manual Intel Corporation,1979
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