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 கல்விப் பொதுத் தராதரப் பத்திர (சாதாரண தர)ப் பரீட்சை, 2015 டிசெம்பர்  
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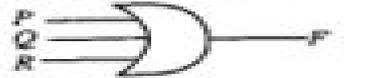
අනෙවැනුණු හා සන්නිවේදන තාක්ෂණය <b>I, II</b> தகவல் தொடர்புமீடல் தொழினுட்பவியல் <b>I, II</b> Information & Communication Technology <b>I, II</b>	மூன்று மணி Three hours
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### Information & Communication Technology I

**Note :**

- (i) Answer all questions.
- (ii) In each of the questions 1 to 40, pick one of the alternatives (1), (2), (3), (4) which you consider as correct or most appropriate.
- (iii) Mark a cross (X) on the number corresponding to your choice in the answer sheet provided.
- (iv) Further instructions are given on the back of the answer sheet. Follow them carefully.

1. The Central Processing Unit (CPU) of a computer consists of which of the following?
  - (1) Arithmetic and Logic Unit (ALU), Control Unit (CU), display unit
  - (2) Arithmetic and Logic Unit (ALU), Control Unit (CU), input/output devices
  - (3) Arithmetic and Logic Unit (ALU), Control Unit (CU), memory registers
  - (4) Arithmetic and Logic Unit (ALU), Control Unit (CU), secondary storage
  
2. Which of the following is not a potential benefit of a computer network which is connected to the Internet?
  - (1) Backing up files to a file server in another network connected to the Internet
  - (2) Reduced risk of viruses and malware
  - (3) Sharing resources in the network
  - (4) Use of the web
  
3. Consider a logic circuit with three inputs  $P$ ,  $Q$ ,  $R$  and one output  $F$ . The following is observed in this circuit:
  - if any single input is 1, then output  $F$  is 0, irrespective of what the remaining inputs are.
  - if all inputs are 0, then the output  $F$  is 1.
 Which of the following is the most likely logic gate configuration for the circuit?
 

(1) 	(2) 
(3) 	(4) 
  
4. To qualify in an examination, candidates have to pass one compulsory subject  $S_1$  and one of the three optional subjects  $S_2$ ,  $S_3$  and  $S_4$ . Which of the following Boolean expressions correctly represents this scenario?
 

(1) $S_1 \text{ AND } (S_2 \text{ AND } S_3 \text{ AND } S_4)$	(2) $S_1 \text{ AND } (S_2 \text{ OR } S_3 \text{ OR } S_4)$
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