(f) What is floating point number? What is the difference between Single precision and double precision floating point numbers? What is a representation for Zero in IEEE 754 single precision standard. Represent $(-23456.05)_{10}$ and $(0.00025)_{10}$ in IEEE 754 single precision format. (4 Marks)

Question 2 (Covers Block 2)

(a) A RAM has 1M rows each having 16 cells:
    (i) How many data input and data output lines does this RAM needs? Explain your answer.
    (ii) What is the capacity of RAM in bytes.
    (iii) How many address lines are needed for this RAM? Give reason in support of your answer. (2 Marks)

(b) A computer has 1 MB RAM and has a word size of 16 bits. It has cache memory having 8 blocks having a block size of 32 bits. Show how the main memory address 0001 1001 1110 1101 0001 will be mapped to cache address, if
    (i) Direct cache mapping is used
    (ii) Associative cache mapping is used
    (iii)Two way set associative cache mapping is used. (4 Marks)

(c) What is the need of DMA in a Computer? How is DMA different than that of Interrupt driven I/O technique? Assume that a new computer supports few bytes of input from multiple user's, which of the three I/O techniques is most suitable for this computer? Justify your answer. (4 Marks)

(d) Assume that a disk has 1000 tracks with each track having 64 sectors and each sector is of size 512 K. A file having the name *bcamca.txt* is of size 16 M. Assume that disk has four free continuous clusters of 8 sectors each. How can this file be given the space on the disk? Also show the content of FAT after the space allocation to this file. You may make suitable assumptions. You may assume the cluster size as 2 sectors, if needed. (4 Marks)

(e) Explain the following giving their uses and advantages/disadvantages.
    (Word limit for answer of each part is 50 words ONLY) (6 Marks)
    (i)   Memory Interleaving
    (ii)   RAID
    (iii)   IDE interface
    (iv)   CAV and CLV in the context of disks
    (v)   Passive and Active matrix display
    (vi)   Advantages and disadvantages of Ink-jet printers

Question 3 (Covers Block 3)

(a) A computer has a single core processor having 8 General purpose registers and 8 additional special purpose registers. The machine has 64 KB RAM. The size of each register and memory word is 16 bits each. An instruction of the machine is of fixed length and is equal to two memory words. Each instruction of the machine has two operands – one memory operand and second register operand. Memory

5

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