



Contemporary Logic Design

Randy H. Katz
University of California
Benjamin Cummings/Addison Wesley Publishing Company
1993

Table of Contents

1. [Introduction](#)
2. [Two-Level Combinational Logic](#)
3. [Multilevel Combinational Logic](#)
4. [Programmable and Steering Logic](#)
5. [Arithmetic Circuits](#)
6. [Sequential Logic Design](#)
7. [Sequential Logic Case Studies](#)
8. [Finite State Machine Design](#)
9. [Finite State Machine Optimization](#)
10. [Finite State Machine Implementation](#)
11. [Computer Organization](#)
12. [Controller Implementation](#)

[Appendix A: Number Systems](#)

[Appendix B: Basic Electronic Components](#)

Other Useful Links

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Randy H. Katz, Gaetano Borriello



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Embedded SoPC Design with Nios II Processor and Verilog Examples Pong P. Chu, 2012-05-14 Explores the unique hardware programmability of FPGA based embedded systems using a learn by doing approach to introduce the concepts and techniques for embedded SoPC design with Verilog An SoPC system on a programmable chip integrates a processor memory modules I O peripherals and custom hardware accelerators into a single FPGA field programmable gate array device In addition to the customized software customized hardware can be developed and incorporated into the embedded system as well allowing us to configure the soft core processor create tailored I O interfaces and develop specialized hardware accelerators for computation intensive tasks Utilizing an Altera FPGA prototyping board and its Nios II soft core processor Embedded SoPC Design with Nios II Processor and Verilog Examples takes a learn by doing approach to illustrate the hardware and software design and development process by including realistic projects that can be implemented and tested on the board Emphasizing hardware design and integration throughout the book is divided into four major parts Part I covers HDL and synthesis of custom hardware Part II introduces the Nios II processor and provides an overview of embedded software development Part III demonstrates the design and development of hardware and software of several complex I O peripherals including a PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides several case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology While designing and developing an embedded SoPC can be rewarding the learning can be a long and winding journey This book shows the trail ahead and guides readers through the initial steps to exploit the full potential of this emerging methodology

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FPGA devices Nios II soft core processor and development platform from Altera Co which is one of the two main FPGA manufactures Altera has a generous university program that provides free software and discounted prototyping boards for educational institutions details at www.altera.com/university The two main educational prototyping boards are known as DE1 99 and DE2 269 All experiments can be implemented and tested with these boards A board combined with this book becomes a turn key solution for the SoPC design experiments and projects Most HDL and C codes in the book are device independent and can be adapted by other prototyping boards as long as a board has similar I O configuration

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Table of Contents Contemporary Logic Design Solution Manual

1. Understanding the eBook Contemporary Logic Design Solution Manual
 - The Rise of Digital Reading Contemporary Logic Design Solution Manual
 - Advantages of eBooks Over Traditional Books
2. Identifying Contemporary Logic Design Solution Manual
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Contemporary Logic Design Solution Manual
 - User-Friendly Interface
4. Exploring eBook Recommendations from Contemporary Logic Design Solution Manual
 - Personalized Recommendations
 - Contemporary Logic Design Solution Manual User Reviews and Ratings
 - Contemporary Logic Design Solution Manual and Bestseller Lists
5. Accessing Contemporary Logic Design Solution Manual Free and Paid eBooks

- Contemporary Logic Design Solution Manual Public Domain eBooks
 - Contemporary Logic Design Solution Manual eBook Subscription Services
 - Contemporary Logic Design Solution Manual Budget-Friendly Options
6. Navigating Contemporary Logic Design Solution Manual eBook Formats
 - ePub, PDF, MOBI, and More
 - Contemporary Logic Design Solution Manual Compatibility with Devices
 - Contemporary Logic Design Solution Manual Enhanced eBook Features
 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Contemporary Logic Design Solution Manual
 - Highlighting and Note-Taking Contemporary Logic Design Solution Manual
 - Interactive Elements Contemporary Logic Design Solution Manual
 8. Staying Engaged with Contemporary Logic Design Solution Manual
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Contemporary Logic Design Solution Manual
 9. Balancing eBooks and Physical Books Contemporary Logic Design Solution Manual
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Contemporary Logic Design Solution Manual
 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
 11. Cultivating a Reading Routine Contemporary Logic Design Solution Manual
 - Setting Reading Goals Contemporary Logic Design Solution Manual
 - Carving Out Dedicated Reading Time
 12. Sourcing Reliable Information of Contemporary Logic Design Solution Manual
 - Fact-Checking eBook Content of Contemporary Logic Design Solution Manual
 - Distinguishing Credible Sources
 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development

- Exploring Educational eBooks
14. Embracing eBook Trends
- Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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