

Jurij Šilc
Borut Robič
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Processor Architecture

From Dataflow to Superscalar and Beyond



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Processor Architecture From Dataflow To Superscalar And Beyond:

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Performance and Simulation Methodology Joshua Jeffrey Yi, 2003 **Handbook of Signal Processing Systems** Shuvra S. Bhattacharyya, Ed F. Deprettere, Rainer Leupers, Jarmo Takala, 2013-06-20 Handbook of Signal Processing Systems is organized in three parts The first part motivates representative applications that drive and apply state of the art methods for design and implementation of signal processing systems the second part discusses architectures for implementing these applications the third part focuses on compilers and simulation tools describes models of computation and their associated design tools and methodologies This handbook is an essential tool for professionals in many fields and researchers of all

levels 25th Euromicro Conference: Workshop on Music Technology and Audio Processing. Workshop on Dependable Computing Systems. Workshop on Software Process and Product Improvement. Workshop on Multimedia and Telecommunications. Special session on network computing ,1999 EUROMICRO 99, Proceedings of the 25th EUROMICRO Conference ,1999 Annotation This is a two volume set of the proceedings of the September 1999 conference on the current and future developments in informatics theories and application areas Volume I 80 contributions discusses digital system design architectures and methods and tools Volume II 30 contributions covers music technology and audio processing dependable computing systems software process and product improvement multimedia and telecommunication and network computing Lacks a subject index Annotation copyrighted by Book News Inc Portland OR **In-vehicle Software & Hardware Systems** ,2006 **Proceedings** ,2003 Books In Print 2004-2005 Ed Bowker Staff,Staff Bowker, Ed,2004

Superscalar Microprocessor Design Mike Johnson,1991 The term superscalar describes a computer architecture that achieves performance by concurrent execution of scalar instructions Superscalar architectures represent the next step in the evolution of microprocessors This book is intended as a technical tutorial and introduction for engineers computer scientists The book concentrates on reduced instruction set RISC processors **Studies in Communication Networks and Superscalar Architecture** George Edward Daddis,1990 *Informatica* ,1999 The 25th Annual International Symposium on Microarchitecture ,1992 **American Book Publishing Record** ,1999 *The Multiscalar Architecture* Manoj Franklin,1993 ACM SIGPLAN Notices ,2004-11 *Conference Proceedings of the 1995 International Conference on Supercomputing* ,1995 Slovenska bibliografija ,1999 PC Magazine ,1995

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