

PART - B

- 5 a. Consider Darlington emitter follower circuit. Obtain expressions for R_{i2} , R_{i1} , A_{i2} , A_{i1} . Compare R_i and A_i of each and overall stage if $R_L = 3.3 \text{ k}\Omega$, $h_{ie} = 1.1 \text{ k}\Omega$, $h_{fe} = 2.5 \times 10^3$, $h_{ic} = 50$ and $h_{oc} = 25 \text{ }\mu\text{A/V}$. (10 Marks)
- b. For a voltage series feedback amplifier topology, obtain expression for A_v and R_{if} . Also explain the principle of voltage amplifier used in feedback amplifiers. (10 Marks)
- 6 a. Derive an expression for second harmonic distortion in power amplifiers, using 3-point method. (10 Marks)
- b. A complementary symmetry push pull amplifier is operated with $V_{CC} = \pm 10\text{V}$, $R_L = 5\Omega$. Determine maximum output power, power rating of transistors and DC input power. (10 Marks)
- 7 a. Explain the concept of positive feedback used in oscillators. (05 Marks)
- b. Obtain an expression for frequency of oscillation in Colpitt's oscillator. (10 Marks)
- c. In a RC phase shift oscillator using transistor, $f_o = 10 \text{ kHz}$, $R_1 = 25 \text{ k}\Omega$, $R_2 = 57 \text{ k}\Omega$, $R_3 = 20 \text{ k}\Omega$, $R = 7.1 \text{ k}\Omega$ and $h_{ie} = 1.8 \text{ k}\Omega$. Calculate the capacitor C and h_{fe} . Draw the circuit diagram. (05 Marks)
- 8 a. Consider a n-channel JFET using voltage divider bias. Explain its DC analysis. Also derive an expression for transconductance g_m . (10 Marks)
- b. Design a fixed bias circuit of Fig. Q8 (b) to have ac gain of -15 . Calculate the value of R_D to get this gain, if $V_{DD} = 40 \text{ V}$, $R_G = 10 \text{ M}\Omega$, $I_{DSS} = 10 \text{ mA}$, $V_p = -4 \text{ V}$, $Y_{os} = 20 \text{ }\mu\text{s}$, $C_1 = 0.1 \text{ }\mu\text{F}$. (10 Marks)

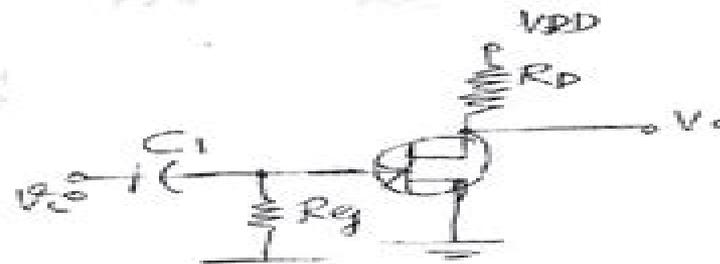


Fig. Q8 (b)

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