

Benefits of Key Value SSD

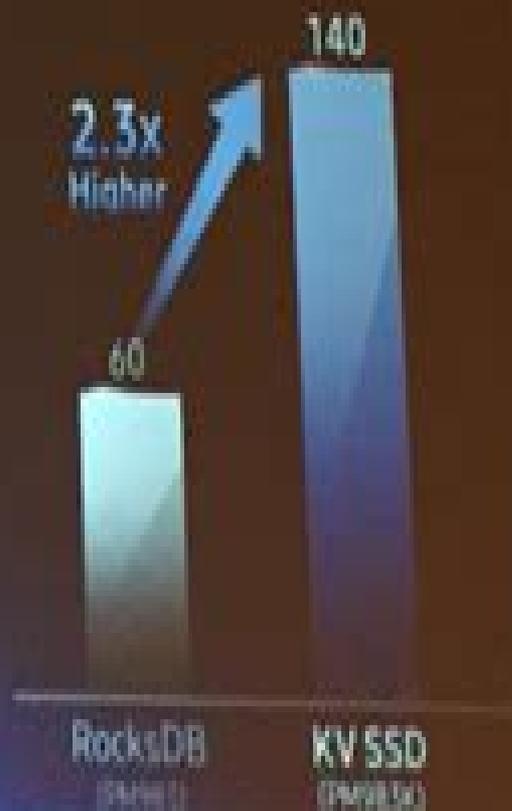
Performance Scaling



Write Amplification



Performance per SSD



Samsung Key Value Ssd Enables High Performance Scaling

Dipti Shankar



Samsung Key Value Ssd Enables High Performance Scaling:

In-/Near-Memory Computing Daichi Fujiki,Xiaowei Wang,Arun Subramaniyan,Reetuparna Das,2022-05-31 This book provides a structured introduction of the key concepts and techniques that enable in near memory computing For decades processing in memory or near memory computing has been attracting growing interest due to its potential to break the memory wall Near memory computing moves compute logic near the memory and thereby reduces data movement Recent work has also shown that certain memories can morph themselves into compute units by exploiting the physical properties of the memory cells enabling in situ computing in the memory array While in and near memory computing can circumvent overheads related to data movement it comes at the cost of restricted flexibility of data representation and computation design challenges of compute capable memories and difficulty in system and software integration Therefore wide deployment of in near memory computing cannot be accomplished without techniques that enable efficient mapping of data intensive applications to such devices without sacrificing accuracy or increasing hardware costs excessively This book describes various memory substrates amenable to in and near memory computing architectural approaches for designing efficient and reliable computing devices and opportunities for in near memory acceleration of different classes of applications

Computerworld ,2006-08-28 For more than 40 years Computerworld has been the leading source of technology news and information for IT influencers worldwide Computerworld s award winning Web site Computerworld com twice monthly publication focused conference series and custom research form the hub of the world s largest global IT media network

Designing Fast, Resilient and Heterogeneity-aware Key-value Storage on Modern HPC Clusters Dipti Shankar,2019 With the recent emergence of in memory computing for Big Data analytics memory centric and distributed key value storage has become vital to accelerating data processing workloads in high performance computing HPC and data center environments This has led to several research works focusing on advanced key value store designs with Remote Direct Memory Access RDMA and hybrid DRAM NVM storage designs However these existing designs are constrained by the blocking store retrieve semantics incurring additional complexity with the introduction of high data availability and durability requirements To cater to the performance scalability durability and resilience needs of the diverse key value store based workloads e g online transaction processing offline data analytics etc it is therefore vital to fully exploit resources on modern HPC systems Moreover to maximize server scalability and end to end performance it is necessary to focus on designing an RDMA aware communication engine that goes beyond optimizing the key value store middleware for better client side latencies Towards addressing this in this dissertation we present a holistic approach to designing high performance resilient and heterogeneity aware key value storage for HPC clusters that encompasses 1 RDMA enabled networking 2 high speed NVMs 3 emerging byte addressable persistent memory devices and 4 SIMD enabled multi core CPU compute capabilities We first introduce non blocking API extensions to the RDMA Memcached client that allows an application to separate the request issue and

completion phases This facilitates overlapping opportunities by truly leveraging the one sided characteristics of the underlying RDMA communication engine while conforming to the basic Set Get semantics Secondly we analyze the overhead of employing memory efficient resilience via Erasure Coding EC in an online fashion Based on this we extend our proposed RDMA aware key value store that supports non blocking API semantics to enable overlapping the EC encoding decoding compute phases with the scatter gather communication protocol involved in resiliently storing the distributed key value data objects This work also examines durable key value store designs for emerging persistent memory technologies While RDMA based protocols employed in existing volatile DRAM based key value stores can be directly leveraged we find that there is a need for a more integrated approach to fully exploit the fine grained durability of these new byte addressable storage devices We propose RDMP KV that employs a hybrid server reply server bypass approach to durably store individual key value pair objects on the remote persistent memory equipped servers via RDMA RDMP KV s runtime can easily adapt to existing server assisted durability and emerging appliance durability RDMA capable interconnects while ensuring server scalability and remote data consistency Finally the thesis explores SIMD accelerated CPU centric hash table designs that can enable higher server throughput We propose an end to end SIMD aware key value store design SCOR KV which introduces optimistic RDMA SIMD aware client centric request response offloading protocols SCOR KV can minimize the server side data processing overheads to achieve better scalability without compromising on the client side latencies With this as the basis we demonstrate the potential performance gains of the proposed designs with online e g YCSB and offline e g in memory and distributed burst buffer over Lustre for Hadoop I O workloads on small scale and production scale HPC clusters

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