(7M,Apr'18)

24. 12. What do you mean by multiprogramming? (3M, Nov'17)

25. 2. Give the major characteristics of RISC and CISC architectures. (8M, Nov'2017)

26. Demonstrate the procedure for obtaining product-of-sums using k-maps? (8M, Nov'17)

27. What is the role of Processor clock, clock rate in the performance of computer system? Explain. (7M, Apr'18)

28.a)Role of MDR in fetching a word from memory. (4M -, Apr'2018)

29. c) Block diagram of a complete processor . ((5M, Apr'2018)

30. What is the use of pipelining and superscalar operations? (3M, Apr'18)

UNIT-2: Machine Instruction and Programs

1. What do you mean by register transfer language? What are the uses of register transfer language? (4M, Nov'17)

2. Describe the phases of instruction cycle briefly. (5M, Nov'17)

3. What are the basic differences among a branch instruction, a call subroutine instruction, and program interrupt? (8M, Nov'17)

4. Write short notes on additional addressing mode. (2M, Apr'18)

5. Describe the basic Instruction format. (4M, Nov'17)

6. Construct a bidirectional shift register with parallel load and give the function table of the circuit.(OS) (8M, Nov'17)

8. An 8-bit register contains the binary value 10011100. What is the register value after arithmetic shift right? Starting from the initial number 10011100, determine the register value after an arithmetic shift left, and state whether there is an overflow? (8M, Nov'17)

10. Using a 4-bit counter with parallel load and a 4-bit adder, draw a block diagram that shows how to implement the following statements:

        x: R1 ←R1+R2   Add R2 to R1

      x'y: R1← R1+1   Increment R1

   where R1 is a counter with parallel load and R2 is a 4-bit register. (8M, Nov'17)

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