

**EXERCISE: COMBINATIONAL CIRCUIT DESIGN:**

This exercise will take you through the full process of logic design from truth table to K-map minimization to implementation.

The objective of this exercise is to design a combinational circuit that is used to control a seven-segment display of decimal digits as shown in Figure 1.

The block diagram of the circuit is also given (Figure 2). The circuit has four inputs a, b, c, d and seven outputs A, B, C, D, E, F, G.

The input to the circuit is a Binary Coded Decimal (BCD) number, that is, abcd = 0000 represents decimal 0, 0001 represents decimal 1, 0010 is 2, 0011 is 3, 0100 is 4, 0101 is 5, 0110 is 6, 0111 is

7, 1000 is 8 and 1001 represents 9. The seven output lines, A, B, ..., G, each connected to one of the seven displays. Based on the input, the appropriate segments should "light up" showing the corresponding decimal number. For example, when the input is 0000, the outputs A, B, C, D, E and F are 1, while the output G is 0. Thus the output displays ZERO. (Figure 3).

The above reasoning shows you how to write the truth table. The first row is given below:

a	b	c	d	A	B	C	D	E	F	G
0	0	0	0	1	1	1	1	1	1	0

Figure 1: Seven Segment Display outputs

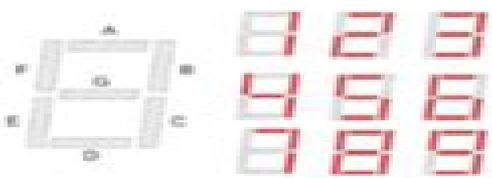


Figure 2: Block Diagram



Figure 3: Display of ZERO.

# Exercise 4 Combinational Circuit Design

**Matthew Justice**



#### **Exercise 4 Combinational Circuit Design:**

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