Mohit Arora

The Art
of Hardware
Architecture

Design Methods and Techniques
for Digital Circuits

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The Art of Hardware Architecture Mohit Arora,2011-10-09 This book highlights the complex issues tasks and skills that must be mastered by an IP designer in order to design an optimized and robust digital circuit to solve a problem The techniques and methodologies described can serve as a bridge between specifications that are known to the designer and RTL code that is final outcome reducing significantly the time it takes to convert initial ideas and concepts into right first time silicon Coverage focuses on real problems rather than theoretical concepts with an emphasis on design techniques across various aspects of chip design **Proceedings of the 2nd Aerospace Frontiers Conference (AFC 2025)** Press of Acta Aero et Astro Sinica,2025-11-08 This book includes original peer reviewed research papers from the 2nd Aerospace Frontiers Conference AFC 2025 held in Beijing China on Apr 11 14 2025 The topics covered include but are not limited to Overall Design and Autonomous Intelligence Technology of Intelligent Unmanned Aerial Vehicles Aerodynamics of Aerospace Vehicles New Aerospace Propulsion Systems Intelligent and Morphing Aircraft Technology Recent Advances and New Challenges in Aircraft Strength Technology Complexity Sciences in Aerospace Intelligent Control and Trajectory Planning of Aerospace Vehicles Frontiers in Low Altitude Traffic Intelligent Sensing and Processing of Aerospace Information Space Intelligent Control Technology Dynamics and Control of Space Unmanned Systems Lightweight Design and Evaluation in Aerospace Equipment making the book a valuable asset for researchers engineers and university students alike **Viruses, Hardware and Software Trojans** Anatoly Belous,Vitali Saladukha,2020-06-27 This book provides readers with a valuable reference on cyber weapons and in particular viruses software and hardware Trojans The authors discuss in detail the most dangerous computer viruses software Trojans and spyware models of computer Trojans affecting computers methods of implementation and mechanisms of their interaction with an attacker a hacker an intruder or an intelligence agent Coverage includes Trojans in electronic equipment such as telecommunication systems computers mobile communication systems cars and even consumer electronics The evolutionary path of development of hardware Trojans from cabinets crates and boxes to the microcircuits IC is also discussed Readers will benefit from the detailed review of the major known types of hardware Trojans in chips principles of their design mechanisms of their functioning methods of their introduction means of camouflaging and detecting as well as methods of protection and counteraction The Art of Software Architecture Stephen T. Albin,2003-03-20 This innovative book uncovers all the steps readers should follow in order to build successful software and systems With the help of numerous examples Albin clearly shows how to incorporate Java XML SOAP ebXML and BizTalk when designing true distributed business systems Teaches how to easily integrate design patterns into software design Documents all architectures in UML and presents code in either Java or C **The Art of Hardware Architecture** ,2011-10-09 Reconfigurable Cryptographic Processor Leibo Liu,Bo Wang,Shaojun Wei,2018-05-16 This book focuses on the design methods for reconfigurable computing processors for cryptographic algorithms It covers the dynamic

reconfiguration analysis of cryptographic algorithms hardware architecture design and compilation techniques for reconfigurable cryptographic processors and also presents a case study of implementing the reconfigurable cryptographic processor Anole designed by the authors team Moreover it features discussions on countermeasures against physical attacks utilizing partially and dynamically reconfigurable array architecture to enhance security as well as the latest trends for reconfigurable cryptographic processors This book is intended for research scientists graduate students and engineers in electronic science and technology cryptography network and information security as well as computer science and technology **The Art of Compiler Design** Thomas Pittman,James F. Peters,1992 Software Programming Languages **Rapid System Prototyping (RSP 2000)** IEEE Computer Society,IEEE Computer Society Staff,2000 The 11th International Workshop on Rapid System Prototyping was held in 2000 These proceedings cover communication and distributed systems reconfigurable architectures partitioning scheduling and performance analysis design methodologies interface technologies and more *The American Architect and the Architectural Review* ,1923 IEEE/ACM/IFIP International Conference on Hardware/Software Codesign & System Synthesis ,2005 First IEEE/ACM/IFIP International Conference on Hardware/Software Codesign & System Synthesis ,2003 The Western Architect ,1909 **Design Methods and Theories** ,1980 Arts & Decoration ,1913 *Reconfigurable and Adaptive Computing* Nadia Nedjah,Chao Wang,2018-10-09 Reconfigurable computing techniques and adaptive systems are some of the most promising architectures for microprocessors Reconfigurable and Adaptive Computing Theory and Applications explores the latest research activities on hardware architecture for reconfigurable and adaptive computing systems The first section of the book covers reconfigurable systems The book presents a software and hardware codesign flow for coarse grained systems on chip a video watermarking algorithm for the H 264 standard a solution for regular expressions matching systems and a novel field programmable gate array FPGA based acceleration solution with MapReduce framework on multiple hardware accelerators The second section discusses network on chip including an implementation of a multiprocessor system on chip platform with shared memory access end to end quality of service metrics modeling based on a multi application environment in network on chip and a 3D ant colony routing 3D ACR for network on chip with three different 3D topologies The final section addresses the methodology of system codesign The book introduces a new software hardware codesign flow for embedded systems that models both processors and intellectual property cores as services It also proposes an efficient algorithm for dependent task software hardware codesign with the greedy partitioning and insert scheduling method GPISM by task graph **Architecture** ,1902 Proceedings ,1982 **Architecture and Building** ,1916 **A Practical Approach to Real-time Systems** Phillip A. Laplante,2000 Under the same cover this volume offers both modern and classic papers focusing on real time systems design and analysis Rather than focusing in theoretical observations of real time systems it is intended for the practical professional who is building real real time systems The editor himself the author of a course on real

time systems has selected articles to provide a deep exploration of issues raised in his other works In particular emphasis is placed on applying practical but theoretically sound approaches in software engineering rate monotonic design and analysis testing and architecting systems for real time applications **Locks and Builders Hardware** Henry Robinson Towne,1904

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