

# Advanced Computer Architecture

- **Systolic Issues**

- **Integration into Existing Systems:**

- System integration is often nontrivial because of the array's high I/O requirements.
    - Often, an additional memory subsystem is added between the existing host and the systolic array to support data access and data multiplexing and de-multiplexing since the existing I/O channel of the host rarely satisfies the bandwidth required by the systolic array.

# S Advanced Computer Architecture Problems And

**Christian Drosten**



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