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# Processor Architecture

From Dataflow to Superscalar and Beyond



Springer

# Processor Architecture From Dataflow To Superscalar And Beyond

**Roman Wölfel**



## **Processor Architecture From Dataflow To Superscalar And Beyond:**

**Processor Architecture** Jurij Silc, Borut Robic, Theo Ungerer, 1999-06-08 A survey of architectural mechanisms and implementation techniques for exploiting fine and coarse grained parallelism within microprocessors Beginning with a review of past techniques the monograph provides a comprehensive account of state of the art techniques used in microprocessors covering both the concepts involved and implementations in sample processors The whole is rounded off with a thorough review of the research techniques that will lead to future microprocessors XXXXXXXX Neuer Text This monograph surveys architectural mechanisms and implementation techniques for exploiting fine grained and coarse grained parallelism within microprocessors It presents a comprehensive account of state of the art techniques used in microprocessors that covers both the concepts involved and possible implementations The authors also provide application oriented methods and a thorough review of the research techniques that will lead to the development of future processors **Parallel and Distributed**

**Processing and Applications** Minyi Guo, 2003 This book constitutes the refereed proceedings of the International Symposium on Parallel and Distributed Processing and Applications ISPA 2003 held in Aizu Japan in July 2003 The 30 revised full papers and 9 revised short papers presented together with abstracts of 4 keynotes were carefully reviewed and selected from numerous submissions The papers are organized in topical sections on applications on Web based and intranet systems compiler and optimization techniques network routing performance evaluation of parallel systems wireless communication and mobile computing parallel topology data mining and evolutionary computing image processing and modeling network security and database and multimedia systems Essentials of Computer Organization and Architecture with Navigate

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**Performance and Simulation Methodology** Joshua Jeffrey Yi, 2003 **Handbook of Signal Processing Systems** Shuvra S. Bhattacharyya, Ed F. Deprettere, Rainer Leupers, Jarmo Takala, 2013-06-20 Handbook of Signal Processing Systems is organized in three parts The first part motivates representative applications that drive and apply state of the art methods for design and implementation of signal processing systems the second part discusses architectures for implementing these applications the third part focuses on compilers and simulation tools describes models of computation and their associated design tools and methodologies This handbook is an essential tool for professionals in many fields and researchers of all

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**Superscalar Microprocessor Design** Mike Johnson,1991 The term superscalar describes a computer architecture that achieves performance by concurrent execution of scalar instructions Superscalar architectures represent the next step in the evolution of microprocessors This book is intended as a technical tutorial and introduction for engineers computer scientists The book concentrates on reduced instruction set RISC processors **Studies in Communication Networks and Superscalar Architecture** George Edward Daddis,1990 *Informatica* ,1999 The 25th Annual International Symposium on Microarchitecture ,1992 **American Book Publishing Record** ,1999 *The Multiscalar Architecture* Manoj Franklin,1993 ACM SIGPLAN Notices ,2004-11 *Conference Proceedings of the 1995 International Conference on Supercomputing* ,1995 Slovenska bibliografija ,1999 PC Magazine ,1995

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