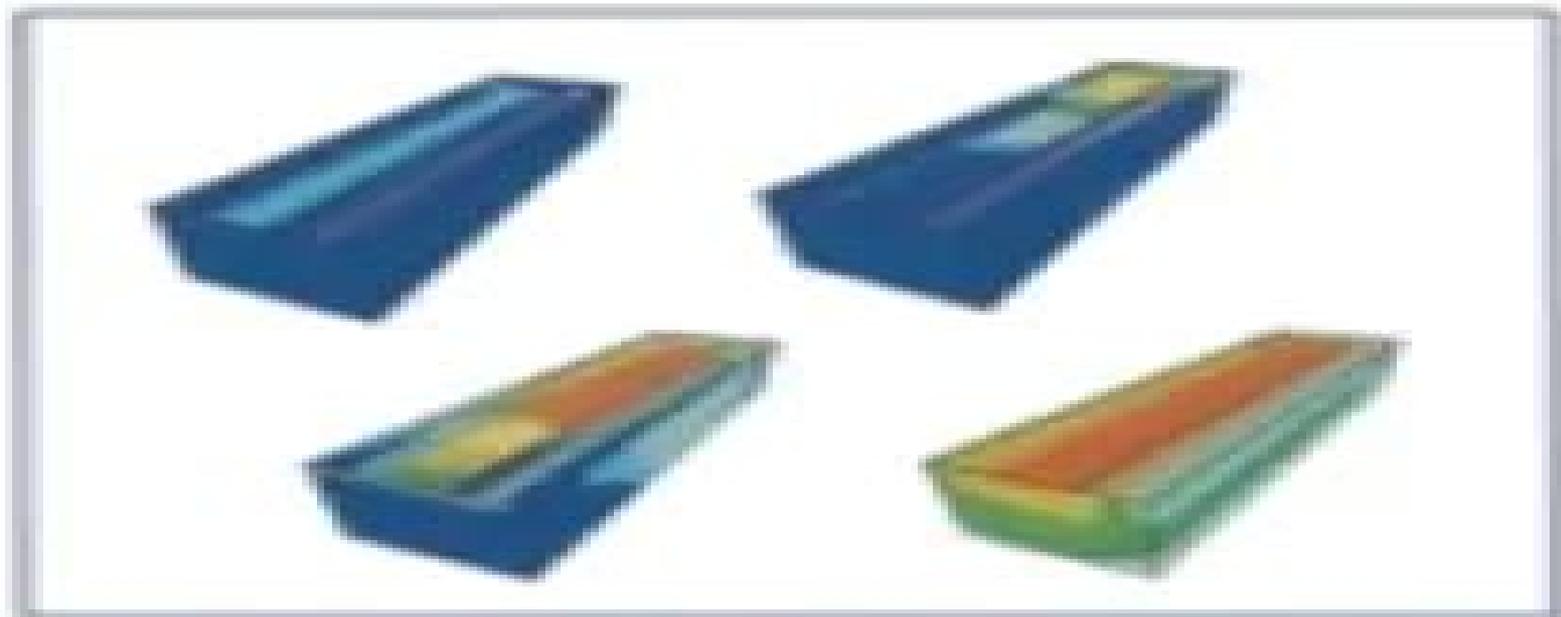




# Advanced Simulation Methods for ESD Protection Development



Kai Ebnark, Harald Gessner & Wolfgang Stadler

# Simulation Methods For ESD Protection Development By Harald Gossner

**Markus P. J. Mergens**



## **Simulation Methods For ESD Protection Development By Harald Gossner:**

**Simulation Methods for ESD Protection Development** Harald Gossner, Kai Esmark, Wolfgang Stadler, 2003-10-16  
Simulation Methods for ESD Protection Development looks at the integration of new techniques into a comprehensive development flow which is now available due to advances made in the field during the recent years. These findings allow for an early stable ESD concept at a very early stage of the technology development which is essential now as development cycles have been reduced. The book also offers ways of increasing the optimization and control of the technology concerning performance, thus making the process more cost-effective and increasingly efficient. This title provides a guide through the latest research and technology, presenting the ESD protection development methodology. This is based on a combination of process, device, and circuit stimulation and addresses the optimization of the industry's critical issue: reduced development cycles. Written to address the needs of the ESD engineer, this text is required reading by all industry practitioners and researchers and students within this field. **THE FIRST** Extensive overview on the subject of ESD simulation. Addresses the industry's critical issue of reduced development cycles and provides solutions. Presents the latest research in the field with high practical relevance and its results.

**System Level ESD Co-Design** Charvaka Duvvury, Harald Gossner, 2015-09-08  
An effective and cost-efficient protection of electronic systems against ESD stress pulses specified by IEC 61000-4-2 is paramount for any system design. This pioneering book presents the collective knowledge of system designers and system testing experts and state-of-the-art techniques for achieving efficient system-level ESD protection with minimum impact on the system performance. All categories of system failures ranging from hard to soft types are considered to review simulation and tool applications that can be used. The principal focus of System Level ESD Co-Design is defining and establishing the importance of co-design efforts from both IC supplier and system builder perspectives. ESD designers often face challenges in meeting customers' system-level ESD requirements, and therefore a clear understanding of the techniques presented here will facilitate effective simulation approaches leading to better solutions without compromising system performance. With contributions from Robert Ashton, Jeffrey Dunnihoo, Micheal Hopkins, Pratik Maheshwari, David Pomerence, Wolfgang Reinprecht, and Matti Usumaki, readers benefit from hands-on experience and in-depth knowledge in topics ranging from ESD design and the physics of system ESD phenomena to tools and techniques to address soft failures and strategies to design ESD-robust systems that include mobile and automotive applications. The first dedicated resource to system-level ESD co-design, this is an essential reference for industry ESD designers, system builders, IC suppliers, and customers, and also Original Equipment Manufacturers (OEMs). Key features: Clarifies the concept of system-level ESD protection. Introduces a co-design approach for ESD-robust systems. Details soft and hard ESD fail mechanisms. Detailed protection strategies for both mobile and automotive applications. Explains simulation tools and methodology for system-level ESD co-design and overviews available test methods and standards. Highlights economic benefits of system ESD co-design.

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**American Book Publishing Record**, 2004 *The British National Bibliography* Arthur James Wells, 2004 **IEEE International Reliability Physics Symposium Proceedings** International Reliability Physics Symposium, 2004 *A Mixed-mode Simulation-design Methodology for On-chip ESD Protection Design* Haigang Feng, 2001 *Device Simulation of ESD Protection Elements* Kai Esmark, 2002 **Simulation of On-chip ESD Protection Networks** Laurence S. Golonka, 1991 *On-chip ESD Protection in Integrated Circuits* Markus P. J. Mergens, 2001 **Modeling, Simulation and Design of EOS/ESD Protection Devices and Circuits in Silicon-on-insulator Technology** Prasun Kumar Raha, 1998 **Characterization, Modeling, and Design of ESD Protection Circuits** Stephen Glen Beebe, 1994

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see the background information section at the beginning of this lab how many neutrons each do

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