

Interview Questions Based on Digital Electronics & Logic Design

1. What is a DFF or D flip flop?
2. How does a synchronous counter work? (Synchronous Counter)
3. What is the difference between a counter and a decoder?
4. In what way are binary and decimal groups?
5. Why is a ring counter?
6. Design a binary to gray code converter.
7. Universal Flip-flop:
 1. What gate forms universal gate?
 2. NAND gate or NOR gate.
 3. NAND gate forms NOR gate.
 4. NOR gate forms NAND gate.
 5. Design a 4-bit gray code binary decoder.
 6. Design a 4-bit binary to 4-bit gray code.
 7. What are universal gates?
 8. What are the two NAND gates or NOR gates?
 9. Design a 4-bit gray code using NAND gates.
 10. Design a 4-bit 8421 gray code using 2:1 MUX.
 11. Design a 4-bit decoder using Schmitt trigger.
 12. What are synchronous counters?
 13. Draw a synchronous counter and derive its equations through Karnaugh map.
 14. Present the operation of a 4-bit shift register.
 15. Why are shift registers used in digital systems?
 16. Design a 4-bit counter using 2:1 MUX.
 17. What are shift registers and why are they used in digital systems?
 18. What are the applications of counters and decoders?
 19. Explain binary arithmetic.
 20. Explain BCD and Binary conversion.

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