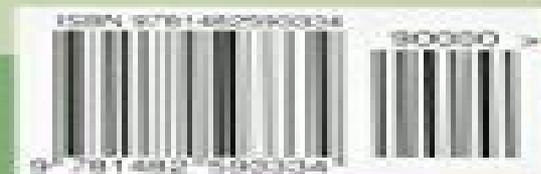


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"This is a valuable addition to the rather scant practical guides available today for digital designers. For those who want to learn from other's experiences without having to make their own mistakes, this book will be immensely valuable. What is impressive about this book is the range covered - 700 pages of insights in various aspects of digital design from the basics of Verilog to complex issues like DMA, arbitration, clock design including practical guide to optimizations for throughput, power, and performance. The book will be indispensable not only to those who are starting out their careers in digital design but also to experienced professionals who are looking for insights in areas they have not worked in before. Highly recommended. Wish more such books by seasoned professionals were available."

Venkatesh Shukla,
EDA Veteran and President, TIE Silicon Valley

The book is intended for digital and system design engineers with emphasis on design and system architecture. The book is broadly divided into two sections - chapters 1 through 10, focusing on the digital design aspects and chapters 11 through 20, focusing on the system aspects of chip design. It comes with real-world examples in Verilog and introduction to SystemVerilog Assertions (SVA).



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