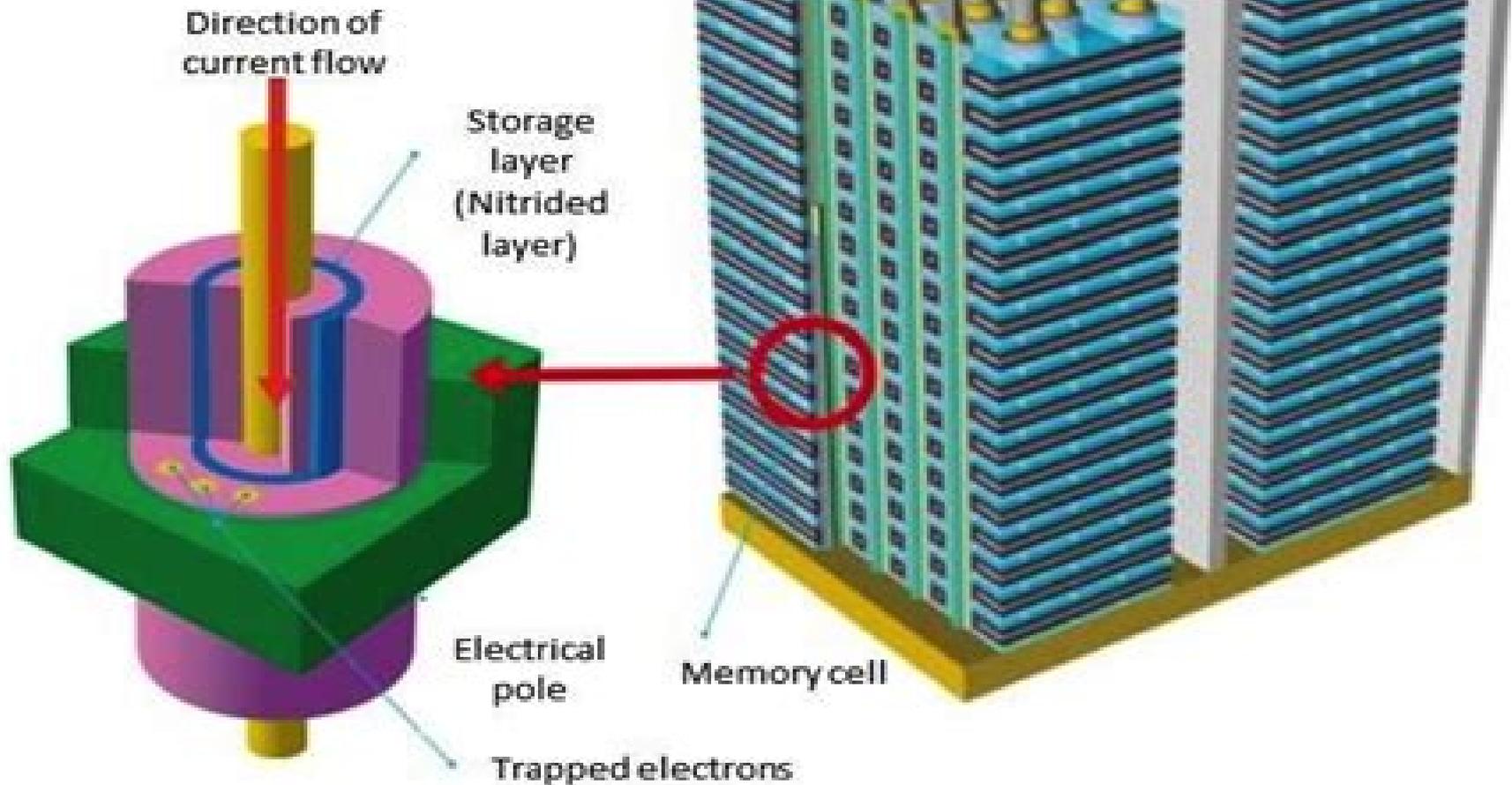


BiCS

Greater capacity because current also flows vertically in a stacked structure



Schematic structure of planar NAND Flash

3d Nand Flash Memory Toshiba

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Vertical 3D Memory Technologies Betty Prince,2014-08-13 The large scale integration and planar scaling of individual system chips is reaching an expensive limit If individual chips now and later terrabyte memory blocks memory macros and processing cores can be tightly linked in optimally designed and processed small footprint vertical stacks then performance can be increased power reduced and cost contained This book reviews for the electronics industry engineer professional and student the critical areas of development for 3D vertical memory chips including gate all around and junction less nanowire memories stacked thin film and double gate memories terrabit vertical channel and vertical gate stacked NAND flash large scale stacking of Resistance RAM cross point arrays and 2 5D 3D stacking of memory and processor chips with through silicon via connections now and remote links later Key features Presents a review of the status and trends in 3 dimensional vertical memory chip technologies Extensively reviews advanced vertical memory chip technology and development Explores technology process routes and 3D chip integration in a single reference Digital Storage in Consumer Electronics Thomas M. Coughlin,2017-12-09 This book provides an introduction to digital storage for consumer electronics It discusses the various types of digital storage including emerging non volatile solid state storage technologies and their advantages and disadvantages It discusses the best practices for selecting integrating and using storage devices for various applications It explores the networking of devices into an overall organization that results in always available home storage combined with digital storage in the cloud to create an infrastructure to support emerging consumer applications and the Internet of Things It also looks at the role of digital storage devices in creating security and privacy in consumer products **Advances in Non-volatile Memory and Storage Technology** Yoshio Nishi,Blanka Magyari-Kope,2019-06-15 Advances in Nonvolatile Memory and Storage Technology Second Edition addresses recent developments in the non volatile memory spectrum from fundamental understanding to technological aspects The book provides up to date information on the current memory technologies as related by leading experts in both academia and industry To reflect the rapidly changing field many new chapters have been included to feature the latest in RRAM technology STT RAM memristors and more The new edition describes the emerging technologies including oxide based ferroelectric memories MRAM technologies and 3D memory Finally to further widen the discussion on the applications space neuromorphic computing aspects have been included This book is a key resource for postgraduate students and academic researchers in physics materials science and electrical engineering In addition it will be a valuable tool for research and development managers concerned with electronics semiconductors nanotechnology solid state memories magnetic materials organic materials and portable electronic devices Discusses emerging devices and research trends such as neuromorphic computing and oxide based ferroelectric memories Provides an overview on developing nonvolatile memory and storage technologies and explores their strengths and weaknesses Examines improvements to flash technology charge trapping and resistive random access memory Seeking

the Truth from Mobile Evidence John Bair,2017-11-17 Seeking the Truth from Mobile Evidence Basic Fundamentals Intermediate and Advanced Overview of Current Mobile Forensic Investigations will assist those who have never collected mobile evidence and augment the work of professionals who are not currently performing advanced destructive techniques This book is intended for any professional that is interested in pursuing work that involves mobile forensics and is designed around the outcomes of criminal investigations that involve mobile digital evidence Author John Bair brings to life the techniques and concepts that can assist those in the private or corporate sector Mobile devices have always been very dynamic in nature They have also become an integral part of our lives and often times a digital representation of where we are who we communicate with and what we document around us Because they constantly change features allow user enabled security and or encryption those employed with extracting user data are often overwhelmed with the process This book presents a complete guide to mobile device forensics written in an easy to understand format Provides readers with basic intermediate and advanced mobile forensic concepts and methodology Thirty overall chapters which include such topics as preventing evidence contamination triaging devices troubleshooting report writing physical memory and encoding date and time stamps decoding Multi Media Messages decoding unsupported application data advanced validation water damaged phones Joint Test Action Group JTAG Thermal and Non Thermal chip removal BGA cleaning and imaging In System Programming ISP and more Popular JTAG boxes Z3X and RIFF RIFF2 are expanded on in detail Readers have access to the companion guide which includes additional image examples and other useful materials

Nanomaterials-Based Charge Trapping Memory Devices Ammar Nayfeh,Nazek El-Atab,2020-05-27 Rising consumer demand for low power consumption electronics has generated a need for scalable and reliable memory devices with low power consumption At present scaling memory devices and lowering their power consumption is becoming more difficult due to unresolved challenges such as short channel effect Drain Induced Barrier Lowering DIBL and sub surface punch through effect all of which cause high leakage currents As a result the introduction of different memory architectures or materials is crucial Nanomaterials based Charge Trapping Memory Devices provides a detailed explanation of memory device operation and an in depth analysis of the requirements of future scalable and low powered memory devices in terms of new materials properties The book presents techniques to fabricate nanomaterials with the desired properties Finally the book highlights the effect of incorporating such nanomaterials in memory devices This book is an important reference for materials scientists and engineers who are looking to develop low powered solutions to meet the growing demand for consumer electronic products and devices Explores in depth memory device operation requirements and challenges Presents fabrication methods and characterization results of new nanomaterials using techniques including laser ablation of nanoparticles ALD growth of nano islands and agglomeration based technique of nanoparticles Demonstrates how nanomaterials affect the performance of memory devices

NAND Flash Memory Technologies Seiichi Aritome,2015-12-29 Offers a comprehensive overview of NAND flash memories with

insights into NAND history technology challenges evolutions and perspectives Describes new program disturb issues data retention power consumption and possible solutions for the challenges of 3D NAND flash memory Written by an authority in NAND flash memory technology with over 25 years experience *Asia Electronics Industry* ,2007 *Malaysian Business* ,1998 **The United States Patents Quarterly** ,2011 Report of cases relating to patents trade marks copyrights decided by Supreme Court of the United States United States Circuit courts of appeals District courts of the United States United States Court of Customs and Patent Appeals Court of Claims of the United States United States Court of Appeals for the District of Columbia Commissioner of Patents and Patent Office Board of Appeals **3D Flash Memories** Rino Micheloni,2016-05-26 This book walks the reader through the next step in the evolution of NAND flash memory technology namely the development of 3D flash memories in which multiple layers of memory cells are grown within the same piece of silicon It describes their working principles device architectures fabrication techniques and practical implementations and highlights why 3D flash is a brand new technology After reviewing market trends for both NAND and solid state drives SSDs the book digs into the details of the flash memory cell itself covering both floating gate and emerging charge trap technologies There is a plethora of different materials and vertical integration schemes out there New memory cells new materials new architectures 3D Stacked BiCS and P BiCS 3D FG 3D VG 3D advanced architectures basically each NAND manufacturer has its own solution Chapter 3 to chapter 7 offer a broad overview of how 3D can materialize The 3D wave is impacting emerging memories as well and chapter 8 covers 3D RRAM resistive RAM crosspoint arrays Visualizing 3D structures can be a challenge for the human brain this is way all these chapters contain a lot of bird s eye views and cross sections along the 3 axes The second part of the book is devoted to other important aspects such as advanced packaging technology i e TSV in chapter 9 and error correction codes which have been leveraged to improve flash reliability for decades Chapter 10 describes the evolution from legacy BCH to the most recent LDPC codes while chapter 11 deals with some of the most recent advancements in the ECC field Last but not least chapter 12 looks at 3D flash memories from a system perspective Is 14nm the last step for planar cells Can 100 layers be integrated within the same piece of silicon Is 4 bit cell possible with 3D Will 3D be reliable enough for enterprise and datacenter applications These are some of the questions that this book helps answering by providing insights into 3D flash memory design process technology and applications F&S Index United States Annual ,1999 *EDN* ,2005 Electrical & Electronics Abstracts ,1997 **PC World** ,2007 *Asian Sources Electronics* ,2006 Predicasts F & S Index ,1992 **Solid State Technology** ,2008 Thermal-Aware Relocation Strategy for Reliable and Efficient 3D NAND Flash Memory □□,2025 Predicasts F & S Index International Annual ,1992 PC Magazine ,1999

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