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Applied Reconfigurable Computing. Architectures, Tools, and Applications Iouliia Skliarova,Piedad Brox Jiménez,Mário Véstias,Pedro C. Diniz,2024-03-11 This LNCS conference volume constitutes the proceedings of the 20th International Symposium ARC 2024 in Aveiro Portugal in March 2024 The 16 full papers together with 5 papers from the technical program included in this volume were carefully reviewed and selected from 24 submissions The conference focuses on the application and development of reconfigurable computing techniques fault tolerance data and graph processing acceleration to computer security   **Euro-Par 2017: Parallel Processing Workshops** Dora B. Heras,Luc Bougé,Gabriele Mencagli,Emmanuel Jeannot,Rizos Sakellariou,Rosa M. Badia,Jorge G. Barbosa,Laura Ricci,Stephen L. Scott,Stefan Lankes,Josef Weidendorfer,2018-02-07 This book constitutes the proceedings of the workshops of the 23rd International Conference on Parallel and Distributed Computing Euro Par 2017 held in Santiago de Compostela Spain in August 2017 The 59 full papers presented were carefully reviewed and selected from 119 submissions Euro Par is an annual international conference in Europe covering all aspects of parallel and distributed processing These range from theory to practice from small to the largest parallel and distributed systems and infrastructures from fundamental computational problems to full edged applications from architecture compiler language and interface design and implementation to tools support infrastructures and application performance aspects   **Applied Reconfigurable Computing. Architectures, Tools, and Applications** Francesca Palumbo,Georgios Keramidas,Nikolaos Voros,Pedro C. Diniz,2023-09-15 This book constitutes the proceedings of the 19th International Symposium on Applied Reconfigurable Computing ARC 2023 which was held in Cottbus Germany in September 2023 The 18 full papers presented in this volume were reviewed and selected from numerous submissions The proceedings also contain 4 short PhD papers The contributions were organized in topical sections as follows Design methods and tools applications architectures special session near and in memory computing and PhD forum papers

Society 5.0: Cyberspace for Advanced Human-Centered Society Alla G. Kravets,Alexander A. Bolshakov,Maxim Shcherbakov,2021-01-04 This book includes new findings on socio economic cyber systems control and development approaches based on AI and data driven techniques The authors determine key social challenges and the main features of management and modeling processes To enhance the efficiency of the socio economic process new approaches were suggested as well as its components based on the concept of Society 5 0 In this book the authors substantiate the scientific practical and methodological approaches to the smart technologies development The core is a human centered concept of cyber systems as well as the intelligent paradigm Approaches combined both mathematical models and data processing techniques are also proposed as advanced methods for smart system design for ecology and health care The implementations of the developed prototypes including testing in real domains which have collected and analyzed big data and proved their effectiveness are presented The target audience of the book are practitioners enterprise representatives scientists and Ph D

and Master students who perform scientific research or applications of socio economic cyber systems in different domains

Applied Reconfigurable Computing Stephan Wong,Antonio Carlos Beck,Koen Bertels,Luigi Carro,2017-03-30 This book constitutes the refereed proceedings of the 13th International Symposium on Applied Reconfigurable Computing ARC 2017 held in Delft The Netherlands in April 2017 The 17 full papers and 11 short papers presented in this volume were carefully reviewed and selected from 49 submissions They are organized in topical sections on adaptive architectures embedded computing and security simulation and synthesis design space exploration fault tolerance FGPA based designs neural neworks and languages and estimation techniques **Machine Learning Models and Architectures for Biomedical Signal Processing** Suman Lata Tripathi,Valentina Emilia Balas,Mufti Mahmud,Soumya Banerjee,2024-11-05 Machine Learning Models and Architectures for Biomedical Signal Processing presents the fundamental concepts of machine learning techniques for bioinformatics in an interactive way The book investigates how efficient machine and deep learning models can support high speed processors with reconfigurable architectures like graphic processing units GPUs Field programmable gate arrays FPGAs or any hybrid system This great resource will be of interest to researchers working to increase the efficiency of hardware and architecture design for biomedical signal processing and signal processing techniques Covers the hardware architecture implementation of machine learning algorithms Discusses the software implementation approach and the efficient hardware of machine learning application with FPGA Presents the major design challenges and research potential in machine learning techniques **High Performance Computing** Michèle Weiland,Guido Juckeland,Sadaf Alam,Heike Jagode,2019-12-02 This book constitutes the refereed post conference proceedings of 13 workshops held at the 34th International ISC High Performance 2019 Conference in Frankfurt Germany in June 2019 HPC I O in the Data Center HPC IODC Workshop on Performance programming models system software and applications solutions for heterogeneity reliability power efficiency of systems virtualization and containerized environments big data and cloud computing and artificial intelligence *FPGA-based Implementation of Signal Processing Systems* Roger Woods,2008-11-24 The automation of the techniques for component architectural synthesis computational models and the reduction of energy consumption to help improve FPGA performance are given in detail FPGA based Implementation of Signal Processing Systems is an important reference for practising engineers and researchers working on the design and development of DSP systems for radio telecommunication information audio visual and security applications Senior level electrical and computer engineering graduates taking courses in signal processing or digital signal processing shall also find this volume of interest BOOK JACKET **14th Symposium on Integrated Circuits and Systems Design** Sociedade Brasileira de Computação,2001 Annotation Papers from a September 2001 symposium report on recent advances in areas of integrated circuits and systems design including embedded systems rapid prototyping formal methods codesign CAD and test analog digital and physical design and low power and low voltage Specific topics include communication architectures for system on chip using the CAN

protocol and reconfigurable computing technology for Web based smart house automation and optimizing BBD based verification analyzing variable dependencies Other subjects include interconnection length estimation at logic level an environment to aid the synthesis of threephase analogue waveform using AHDL and extending sequencing graphs for reconfigurable applications modeling This work lacks a subject index c Book News Inc   <u>Field-programmable Logic and Applications</u> ,2002   **Annual Meeting of the North American Fuzzy Information Processing Society--NAFIPS.** North American Fuzzy Information Processing Society. Annual Meeting,2005   **Computer Design's Electronic Systems Technology & Design** ,1999   *STAR EMC Trigger Level Design for an Orca FPGA* Brian Scott Foulds,2001   *27th EUROMICRO Conference* ,2001 Annotation This proceedings volume contains the papers given by international researchers at the 27th Euromicro conference held in Warsaw in 2001 The conference featured workshops on multimedia and telecommunications software process and product improvement and component based software engineering A sampling of topics includes components for real time systems software reliability network protocols and audio video processing management The volume is not indexed c Book News Inc   *Design and Applications of a Reconfigurable Computing System for High Performance Digital Signal Processing* Chen Chang (Ph.D.),2005   **Publications of the Astronomical Society of Japan** Nihon Tenmon Gakkai,2005   *IEEE Circuits & Devices* ,2006   **Proceedings** ,1994   *Subject Guide to Books in Print* ,1991   **Shortening the Path from Specification to Prototype** ,2002

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