



High Speed Serdes Devices and Applications

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learning convolutional neural networks **VLSI Design and Test** Brajesh Kumar Kaushik, Sudeb Dasgupta, Virendra Singh, 2017-12-21 This book constitutes the refereed proceedings of the 21st International Symposium on VLSI Design and Test VDAT 2017 held in Roorkee India in June July 2017 The 48 full papers presented together with 27 short papers were carefully reviewed and selected from 246 submissions The papers were organized in topical sections named digital design analog mixed signal VLSI testing devices and technology VLSI architectures emerging technologies and memory system design low power design and test RF circuits architecture and CAD and design verification **McGraw-Hill 2005**

Yearbook of Science and Technology McGraw Hill, 2005 Covers over 90 disciplines of science and technology including biomedical science chemistry cosmology information science environmental science and nanotechnology The British National Bibliography Arthur James Wells, 2009 2000 International Symposium on Microelectronics , 2000 This text constitutes proceedings from the International Symposium on Microelectronics that took place in Boston Massachusetts in September 2000 *EDN.* , 2006 **Proceedings of the ... International Symposium on Microelectronics** , 2000

IEEE Workshop on High Performance Switching and Routing , 2005 *Display Technologies and Applications for Defense, Security, and Avionics* John T. Thomas, Andrew Malloy, 2007 Proceedings of SPIE present the original research papers presented at SPIE conferences and other high quality conferences in the broad ranging fields of optics and photonics These books provide prompt access to the latest innovations in research and technology in their respective fields Proceedings of SPIE are among the most cited references in patent literature **Network Processors** Panos C. Lekkas, 2003-07-28

Table of contents *High-speed Optical Transceivers* Yuyu Liu, 2006 This book explores the unique advantages and large inherent transmission capacity of optical fiber communication systems The long term and high risk research challenges of optical transceivers are analyzed with a view to sustaining the seemingly insatiable demand for bandwidth A broad coverage of topics relating to the design of high speed optical devices and integrated circuits oriented to low power low cost and small area is discussed Written by specialists with many years of research and engineering experience in the field of optical fiber communication this book is essential for an audience dedicated to the development of integrated electronic systems for optical communication applications It can also be used as a supplementary text for graduate courses on optical transceiver IC design Contents Design Considerations for Integrated Modulator Drivers in Silicon Germanium Technology S Pavan et al Compact Low Noise Pulse Generating Lasers with Repetition Rates of 10 to 50 GHz G Spuehler et al Integrated Wide Band CMOS Duobinary Transmitter for Optical Communication Systems R Tao A 10 Gb/s Equalizer with Integrated Clock and Data Recovery for Optical Communication Systems D S McPherson et al Equalizer Architectures for 40 Gb/s Optical Systems Limited by Polarization Mode Dispersion J Sewter Trade offs in High Speed Serial Link ICs S Li High speed Architectures and Building Blocks for Clock and Data Recovery Systems C S Vaucher et al MOS Current Mode Logic Circuits Design Considerations in High Speed Low Power Applications and Future Trends a Tutorial Y Liu et al Recent Progress in 40 to 100

Gbit/s Class Optical Communications ICs Using InP Based HBT Technologies K Ishii et al 40 Gb/s TDM System Transceiver Prototype in InP HBT Technology K Krishnamurthy et al Enhanced Network Signaling for 10 Gigabit Ethernet to Achieve a LAN/WAN Seamless Interface and Its Implementation in the PHY LSI Transceiver Module H Ichino et al Key Features Wide range of topics relating to the design of high speed optical transceivers Analysis of the long term risks and hard going research challenges of optical transceivers from professionals Useful for readers dedicated to the development of integrated electronic systems for optical communication applications Readership Engineers researchers practitioners academics upper level and undergraduates in integrated circuits and device design in the fields of electronic engineering semiconductor physics and microelectronics **IBM Journal of Research and Development** ,2005 **Design News** ,2002 Electrical Performance of Electronic Packaging ,2003 **2002 IEEE Nuclear Science Symposium** ,2003 **Complete Digital Design : A Comprehensive Guide to Digital Electronics and Computer System Architecture** Mark Balch,2003-06-20 This is a readable hands on self tutorial through basic digital electronic design methods The format and content allows readers faced with a design problem to understand its unique requirements and then research and evaluate the components and technologies required to solve it Begins with basic design elements and expands into full systems Covers digital analog and full system designs Features real world implementation of complete digital systems Mergent Corporate News Reports Monthly ,2003-05 **Electronic Design** ,2005

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