

00	ADD		Eb	Gb	39	CMP		Ev	Gv
01	ADD		Ev	Gv	3A	CMP		Gb	Gv
02	ADD		Gb	Eb	3B	CMP		Gv	Ev
03	ADD		Gv	Ev	3C	CMP		AL	Ib
04	ADD		AL	Ib	3D	CMP		eAX	Iv
05	ADD		eAX	Iv	3E	DS:			
06	PUSH	ES			3F	AAS			
07	POP		ES		40	INC		eAX	
08	OR		Eb	Gb	41	INC		eCX	
09	OR		Ev	Gv	42	INC		eDX	
0A	OR		Gb	Eb	43	INC		eBX	
0B	OR		Gv	Ev	44	INC		eSP	
0C	OR		AL	Ib	45	INC		eBP	
0D	OR		eAX	Iv	46	INC		eSI	
0E	PUSH	CS			47	INC		eDI	
0F	--				48	DEC		eAX	
10	ADC		Eb	Gb	49	DEC		eCX	
11	ADC		Ev	Gv	4A	DEC		eDX	
12	ADC		Gb	Eb	4B	DEC		eBX	
13	ADC		Gv	Ev	4C	DEC		eSP	
14	ADC		AL	Ib	4D	DEC		eBP	
15	ADC		eAX	Iv	4E	DEC		eSI	
16	PUSH	SS			4F	DEC		eDI	
17	POP		SS		50	PUSH	eAX		
18	SBB		Eb	Gb	51	PUSH	eCX		
19	SBB		Ev	Gv	52	PUSH	eDX		
1A	SBB		Gb	Eb	53	PUSH	eBX		
1B	SBB		Gv	Ev	54	PUSH	eSP		
1C	SBB		AL	Ib	55	PUSH	eBP		
1D	SBB		eAX	Iv	56	PUSH	eSI		
1E	PUSH	DS			57	PUSH	eDI		
1F	POP		DS		58	POP		eAX	
20	AND		Eb	Gb	59	POP		eCX	
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22	AND		Gb	Eb	5B	POP		eBX	
23	AND		Gv	Ev	5C	POP		eSP	
24	AND		AL	Ib	5D	POP		eBP	
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26	ES:				5F	POP		eDI	
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28	SUB		Eb	Gb	61	--			
29	SUB		Ev	Gv	62	--			
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30	XOR		Eb	Gb	69	--			
31	XOR		Ev	Gv	6A	--			
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