

Chapter 8

Semiconductor Memories

(based on Kang, Leblebici. *CMOS Digital Integrated Circuits*)

8.1 General concepts

- Data storage capacity available on a single integrated circuit grows exponentially being doubled approximately every two years.
- Capacity of the dynamic read/write memory (DRAM) chip exceeds now 1 Gigabit.
- Data transfer speed of a standard DRAM is at the level of 200Mb/sec/pin.
- Static and dynamic power consumption is of the order of

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Marco Casella



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