



# UNIVERSITY GRANTS COMMISSION

## VACANCY

The University Grants Commission being the apex body of the University System will entertain applications from suitably qualified candidates for the following post at the Secretariat.

### **1. POST OF COMPUTER PROGRAMMER**

#### **Qualifications :**

- (i) Bachelors Degree of a recognized University  
and
- (ii) Ability to programme computers in one or more languages  
i.e, Microsoft, NET, PHP, Python, C/C++  
and  
Thorough knowledge in SQL using Database Management Systems such as MYSQL  
and MS SQL.

**Salary Scale :** U - AS 1 (II) – Rs. 41,095 - 18 x 660 – Rs. 52,975/- p.m.  
(Other government approved allowances will be paid)

#### **Method of**

**Recruitment :** Selection will be made by an aptitude test and interview

### **OTHER BENEFITS APPLICABLE TO THE POST:**

#### **(i) Provident Fund and Contributory Pensions Scheme**

10% and 7% of the salary by the employee and employer respectively, will be credited to the Universities Provident Fund 8% of the salary will be credited to the Pensions Fund by the employer.

#### **(ii) Employees Trust Fund**

3% of the salary by the employer.

#### **(iii) Gratuity**

Payment of Gratuity will be in accordance with the provisions of the Payment of Gratuity Act, No.12 of 1983.

### **APPLICATION PROCEDURE:**

- (i) Duly filled specimen application form should be sent to *Senior Assistant Secretary/ Personnel, University Grants Commission, No: 20, Ward Place, Colombo 07* under registered cover along with copies of relevant certificates to reach on or before **15th May 2024** (Specimen application forms are available on the UGC website, i.e. [www.ugc.ac.lk](http://www.ugc.ac.lk)). Applications sent in the specified form will only be accepted.
- (ii) The name of the post in respect of which the applications are sent, should be stated on the top left hand corner of the envelope.
- (iii) Candidates in the service of the University Grants Commission and Higher Educational Institutions/Institutes, Government Departments, State Corporations and Statutory Boards should channel their applications through the Heads of their respective Institutions. Applications which are not forwarded through the respective Heads of Departments will be rejected.
- (iv). Applications received after the closing date and incomplete applications will not be entertained.
- (v). University Grants Commission reserves the right to shortlist the candidates by following a suitable method.

**Secretary**  
**University Grants Commission**  
**No: 20, Ward Place**  
**Colombo-07.**  
25.04.2024

# Computer System Architecture University Grants Commission

**RS Peters**



## **Computer System Architecture University Grants Commission:**

VLSI Circuits and Embedded Systems Hafiz Md. Hasan Babu, 2022-07-29 Very Large Scale Integration VLSI creates an integrated circuit IC by combining thousands of transistors into a single chip While designing a circuit reduction of power consumption is a great challenge VLSI designs reduce the size of circuits which eventually reduces the power consumption of the devices However it increases the complexity of the digital system Therefore computer aided design tools are introduced into hardware design processes Unlike the general purpose computer an embedded system is engineered to manage a wide range of processing tasks Single or multiple processing cores manage embedded systems in the form of microcontrollers digital signal processors field programmable gate arrays and application specific integrated circuits Security threats have become a significant issue since most embedded systems lack security even more than personal computers Many embedded systems hacking tools are readily available on the internet Hacking in the PDAs and modems is a pervasive example of embedded systems hacking This book explores the designs of VLSI circuits and embedded systems These two vast topics are divided into four parts In the book s first part the Decision Diagrams DD have been covered DDs have extensively used Computer Aided Design CAD software to synthesize circuits and formal verification The book s second part mainly covers the design architectures of Multiple Valued Logic MVL Circuits MVL circuits offer several potential opportunities to improve present VLSI circuit designs The book s third part deals with Programmable Logic Devices PLD PLDs can be programmed to incorporate a complex logic function within a single IC for VLSI circuits and Embedded Systems The fourth part of the book concentrates on the design architectures of Complex Digital Circuits of Embedded Systems As a whole from this book core researchers academicians and students will get the complete picture of VLSI Circuits and Embedded Systems and their applications

Quantum Biocomputing in Quantum Biology Volume I Hafiz Md. Hasan Babu, 2025-07-01 The book starts with the basics of Quantum Computing Biocomputing Quantum Biology Quantum DNA Computing and DNA Quantum Computing It also discusses the fundamental operations in quantum computing and Biocomputing Different types of quantum arithmetic circuits quantum DNA arithmetic circuits and DNA quantum arithmetic circuits such as basic and universal gate operations half adder full adder half subtractor full subtractor N qubit adders multipliers dividers etc are explained clearly Nuclear Magnetic Resonance NMR NMR relaxation quantum cache memory heat conduction circuit and trap ion are also discussed The readers can get a clear idea about different types of quantum quantum DNA and DNA quantum circuits such as arithmetic combinational sequential memory devices programmable logic devices nano processors and will be able to design their own circuits Then it discusses Heat Measurement Speed Calculation Heat Transfer Data Conversion and Data Management in Quantum Computing and Quantum Biocomputing Quantum DNA Computing and DNA Quantum Computing As a whole this book is a great resource for quantum quantum DNA and DNA Quantum Computing it is the book where computing in quantum biology is introduced for the quantum biology researchers students and academicians This is a novel

approach to writing a book in this field This book quenches the thirst of beginners to advanced level readers **Reversible and DNA Computing** Hafiz M. H. Babu,2020-11-09 Master the subjects of reversible computing and DNA computing with this expert volume Reversible and DNA Computing offers readers new ideas and technologies in the rapidly developing field of reversible computing World renowned researcher and author Hafiz Md Hasan Babu shows readers the fundamental concepts and ideas necessary to understand reversible computing including reversible circuits reversible fault tolerant circuits and reversible DNA circuits Reversible and DNA Computing contains a practical approach to understanding energy efficient DNA computing In addition to explaining the foundations of reversible circuits the book covers topics including Advanced logic design An introduction to the fundamentals of reversible computing Advanced reversible logic synthesis Reversible fault tolerance Fundamentals of DNA computing Reversible DNA logic synthesis DNA logic design This book is perfect for undergraduate and graduate students in the physical sciences and engineering as well as those working in the field of quantum computing It belongs on the bookshelves of anyone with even a passing interest in nanotechnology energy efficient computing and DNA computing *Designing, Engineering, and Analyzing Reliable and Efficient Software* Singh, Hardeep,2013-02-28 Due to the role of software systems in safety critical applications and in the satisfaction of customers and organizations the development of efficient software engineering is essential *Designing Engineering and Analyzing Reliable and Efficient Software* discusses and analyzes various designs systems and advancements in software engineering With its coverage on the integration of mathematics computer science and practices in engineering this book highlights the importance of ensuring and maintaining reliable software and is an essential resource for practitioners professors and students in these fields of study **Proceedings of International Conference on Data, Electronics and Computing** Nibaran Das,Ajoy Kumar Khan,Swagata Mandal,Ondrej Krejcar,Debotosh Bhattacharjee,2024-10-07 This book features high quality peer reviewed research papers presented at the International Conference on Data Electronics and Computing ICDEC 2023 organized by Department of Computer Science cloud computing IoT and smart city AI ML big data and data mining VLSI design antenna and microwave and control State-of-the-art Series on Microelectronics M. Aslam,United Nations Industrial Development Organization,1984 *Advances in Software Engineering, Education, and e-Learning* Hamid R. Arabnia,Leonidas Deligiannidis,Fernando G. Tinetti,Quoc-Nam Tran,2021-09-09 This book presents the proceedings of four conferences The 16th International Conference on Frontiers in Education Computer Science and Computer Engineering STEM FECS 20 The 16th International Conference on Foundations of Computer Science FCS 20 The 18th International Conference on Software Engineering Research and Practice SERP 20 and The 19th International Conference on e Learning e Business Enterprise Information Systems Includes the tracks Computer Engineering STEM Foundations of Computer Science Software Engineering Research and e Learning e Business Enterprise Information Systems Features papers from FECS 20 FCS 20 SERP 20 EEE 20 including one open access chapter **Annual Report for the Year ...** India. University Grants

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