

# CBGS SCHEME



USN

18EC35

## Third Semester B.E. Degree Examination, Aug./Sept.2020 Computer Organization and Architecture

Time: 3 hrs.

Max. Marks: 100

Note: Answer any FIVE full questions, choosing ONE full question from each module.

### Module-1

- 1 a. Explain the operation of computer with neat block diagram. (10 Marks)  
b. Explain computer basic performance equation. (04 Marks)  
c. Explain following with an example : i) Three – address instruction  
ii) Two – address instruction iii) One – address instruction. (06 Marks)

OR

- 2 a. Explain Single –BUS structure in computer. (06 Marks)  
b. Explain system software functions in computer. (06 Marks)  
c. What is Operating system? Explain user program and OS routine sharing the processor. (08 Marks)

### Module-2

- 3 a. Explain Big –Endian and Little –Endian with neat diagram. (08 Marks)  
b. Explain memory operations with examples. (04 Marks)  
c. Explain condition codes with examples. (08 Marks)

OR

- 4 a. Discuss following addressing modes with example :  
i) Immediate ii) Register iii) Direct iv) Indirect v) Index. (10 Marks)  
b. What are assembler directive? Explain any five assembler directives. (10 Marks)

### Module-3

- 5 a. With a neat diagram, explain how to interface printer to the processor. (10 Marks)  
b. Define Interrupt, Point out and explain the various ways of enabling and disabling interrupts. (10 Marks)

OR

- 6 a. Explain the following method of handling interrupts from multiple devices.  
i) Daisy chain method ii) Priority structure. (10 Marks)  
b. Explain operation of DMA with neat diagram. (10 Marks)

### Module-4

- 7 a. Explain internal organization of  $16 \times 8$  memory chip. (10 Marks)  
b. Discuss a single –transistor dynamic memory cell. (06 Marks)  
c. Write a note on Virtual Memory. (04 Marks)

OR

- 8 a. Draw and explain the internal organization of  $2M \times 8$  asynchrononous DRAM Chip. (08 Marks)  
b. Describe the principles of magnetic disk. (06 Marks)  
c. What is mapping? Explain set associative cache mapping techniques. (06 Marks)

### Module-5

- 9 a. Discuss with neat diagram, the single bus organization of data path inside a processor. (10 Marks)  
b. What are the action required to execute a complete instruction ADD(R3), R1. (10 Marks)

OR

- 10 a. Draw and explain multiple bus organization of CPU. (10 Marks)  
b. Draw and explain organization of the control unit to allow conditional branching in the microprogram. (10 Marks)

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# Computer Organization And Architecture Objective Question Answer

**L Reisser**



## **Computer Organization And Architecture Objective Question Answer:**

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ques and get the study notes for your exam prep **Microprocessors and Interfacing Techniques** Swapneel Chandrakant Mhatre,2014-01-04 The book is written as per the syllabus of the subject Microprocessors and Interfacing Techniques for S E Computer Engineering Semester II of University of Pune It focuses on the three main parts in the study of microprocessors the architecture the programming and the system design The 8086 microprocessor is described in detail along with glimpses of 8088 80186 and 80188 microprocessors The various peripheral controllers for 8086 88 are also discussed Other topics that are related to the syllabus but not explicitly mentioned are included in the appendices Key Features Programs are given and the related theory is discussed within the same section thereby maintaining a smooth flow and also eliminating the need for a separate section on the practical experiments for the subject of Microprocessors and Interfacing Laboratory Both DOS based programs as well as kit programs are given Algorithms and flowcharts are given before DOS based programs for easy understanding of the program logic *Computer Organization and Assembly Language Programming for the VAX* G. Michael Schneider,Ronald Davis,Thomas Mertz,1992 **Proceedings** American Society for Engineering Education. Conference,1989 **CoED.** ,1990 **The Principles of Computer Organization** G. Michael Schneider,1985 This text offers both the theoretical machine independent concepts underlying the construction of all computers and a specific introduction to the assembly language and architecture of the widely used PDP 11 It discusses the major functional components of a computer memory ALU Input Output and processor and how they are integrated into a complete computer system The book describes and builds an idealized model of a computer and shows how the theoretical concepts are put into practice in the construction of the PDP It integrates hardware concepts with software principles introducing the student to internal systems programs used to run the computer [Kaplan GRE Exam 2008 Comprehensive Program](#) Kaplan,2007-07-03 Students who turn to Kaplan can depend on the most accurate information on the GRE test change As always Kaplans comprehensive guides combine proven score raising strategies with the latest technology to help

students exceed their expectations *CompTIA Security+ All-in-One Exam Guide, Fifth Edition (Exam SY0-501)* Wm. Arthur Conklin, Greg White, Dwayne Williams, Chuck Cothren, Roger L. Davis, 2018-01-05 This fully updated study guide covers every topic on the current version of the CompTIA Security exam Take the latest version of the CompTIA Security exam with complete confidence using the detailed information contained in this highly effective self study system Written by a team of leading information security experts this authoritative guide addresses the skills required for securing a network and managing risk and enables you to become CompTIA Security certified CompTIA Security All in One Exam Guide Fifth Edition Exam SY0 501 covers all exam domains and features 200 accurate practice questions To aid in study the book features learning objectives at the beginning of each chapter exam tips practice exam questions and in depth explanations All questions mirror those on the live test in style format and difficulty Beyond fully preparing you for the challenging SY0 501 exam the book also serves as a valuable on the job reference for IT professionals Provides 100% coverage of every objective on exam SY0 501 Electronic content includes 200 practice questions and a secured book PDF Written by a team of experienced IT security academics *Distributed Computer Control Systems* Thomas J. Harrison, 1980 Distributed Computer Control Systems Proceedings of the IFAC Workshop Tampa Florida U S A 2 4 October 1979 focuses on the design processes methodologies and applications of distributed computing systems **Computer Science** J. Glenn Brookshear, 1997 An introduction to computer science Using real life analogies and examples this text introduces coverage of the World Wide Web and the Java programming language and includes a larger emphasis on the object oriented paradigm and networking Fourth Workshop on Computer Architecture for Non-numeric Processing, 1978 *International Workshop on Computer Architecture, Grenoble, June 26-28th, 1973*, 1973 Architecture Exam Review: Nonstructural topics David Kent Ballast, 1998 **Architecture Exam Review: Nonstructural topics** Steven E. O'Hara, David Kent Ballast, 2002 The Architect Registration Exam ARE is part of the licensing requirements for U S and Canadian architects A computerized closed book exam the ARE is administered year round at a network of test centers The topics represented on the ARE may be roughly divided into two areas structural and nonstructural We offer two primary study guides for the exam one volume devoted to each area Each volume includes concise reviews of the exam topics with practice problems and solutions Additional practice for the ARE is provided by Architecture Exam Review Site Planning and Building Design Graphic Divisions Volume II Nonstructural Topics provides a thorough review of the ARE nonstructural exam topics including the exam s most challenging divisions Site Planning and Building Design More than 80 sample questions are provided with solutions along with a valuable checklist of items that should be considered in every design solution The book also covers test taking strategy and is enhanced by illustrations figures and tables along with a detailed index The fifth edition was updated to reflect changes in the exam the adoption of a computerized format and use of the 2000 versions of the International Building Code and the International Plumbing Code

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### **Computer Organization And Architecture Objective Question Answer :**

Fuses and relays Honda Airwave (GJ), 2005 - 2010 Sep 24, 2021 — The fuse box is located behind the additional glove compartment. General form. Diagram ... Fuse box diagram Honda Airwave and relay with ... In the passenger compartment, the main fuse and relay box is located at the bottom of the instrument panel on the driver's side, behind a protective cover. Honda In this publication you will find information describing fuses and relays for Honda Avancer with fuse box diagrams, photographs and their locations. Select the ... Fuse Box Diagram Honda Fuse box diagrams (location and assignment of the electrical fuses and relays) Honda. Honda Airwave Owner's Manuals PDF Honda Airwave with a gasoline engine - owner's

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