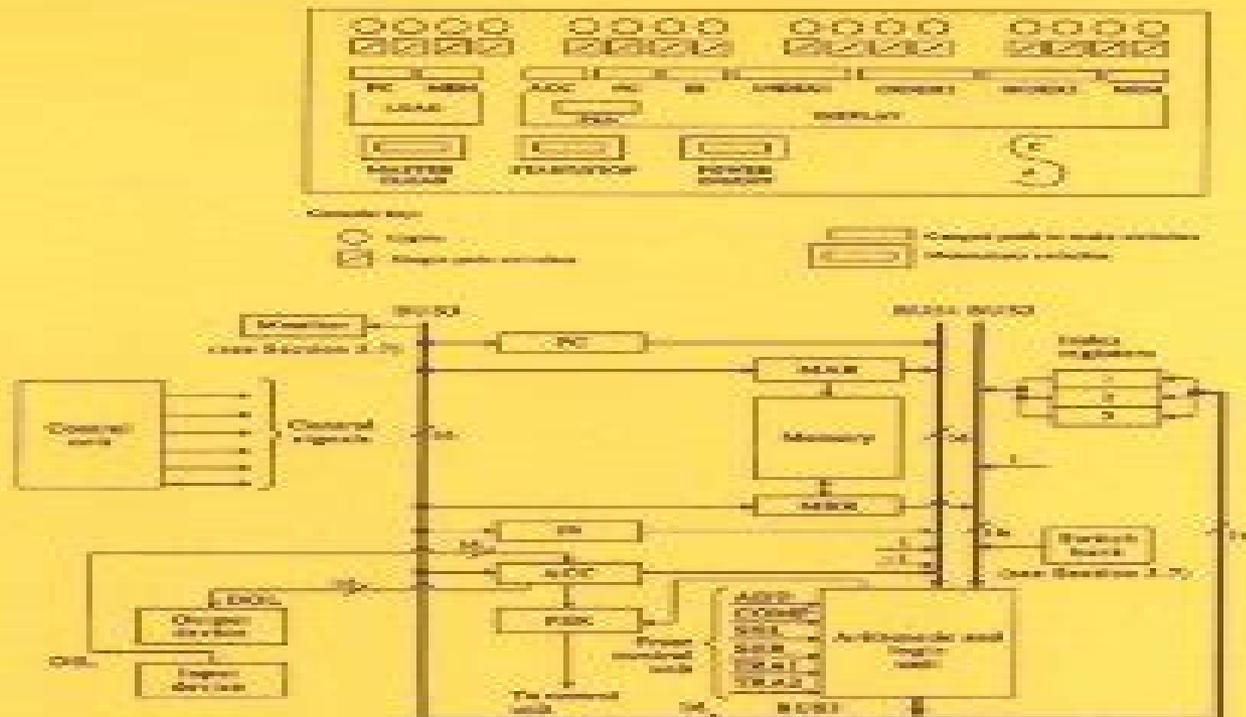


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SAJJAN G. SHIVA

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Computer Organization V. Carl Hamacher, Zvonko G. Vranesic, Safwat G. Zaky, 1996 This text is intended to be of use for first courses in computer architecture taught in computer science and electrical computing engineering departments The fourth edition places more emphasis on design and covers topics such as RISC processors performance analysis and memory systems

Computer Design & Architecture Sajjan G. Shiva, 1991 This unique and classroom proven text provides a hands on introduction to the design of computer systems It

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