

Fourth Edition
CMOS VLSI Design

A CIRCUITS AND SYSTEMS PERSPECTIVE

Neil H. E. Weste | David Money Harris



The fourth edition of this best-selling text discusses modern techniques for the design of complex and high-performance CMOS systems on a chip. Covering the fundamentals of CMOS design from the digital systems level to the circuit level, this book is a guide to good design practices.

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- Broad, up-to-date, and comprehensive coverage of the entire field of CMOS VLSI design
- Introduces each key element of VLSI design, including delay, power, interconnect, and robustness
- Ample circuit-level coverage, emphasizing practical circuits used in commercial chips
- Illustrates circuit simulation with SPICE through a complete tutorial chapter (Chapter 11)
- Presents extensive coverage of data path, array, and special purpose building blocks (Chapters 11–13)
- Contains a rich set of problems, worked examples and exercises for learning reinforcement
- Presents “war stories” of “chips gone bad” and their lessons for today’s designers
- Links theory to practice through expert Historical Perspectives and PMath sections that reveal what’s happening in real VLSI and engineering laboratories

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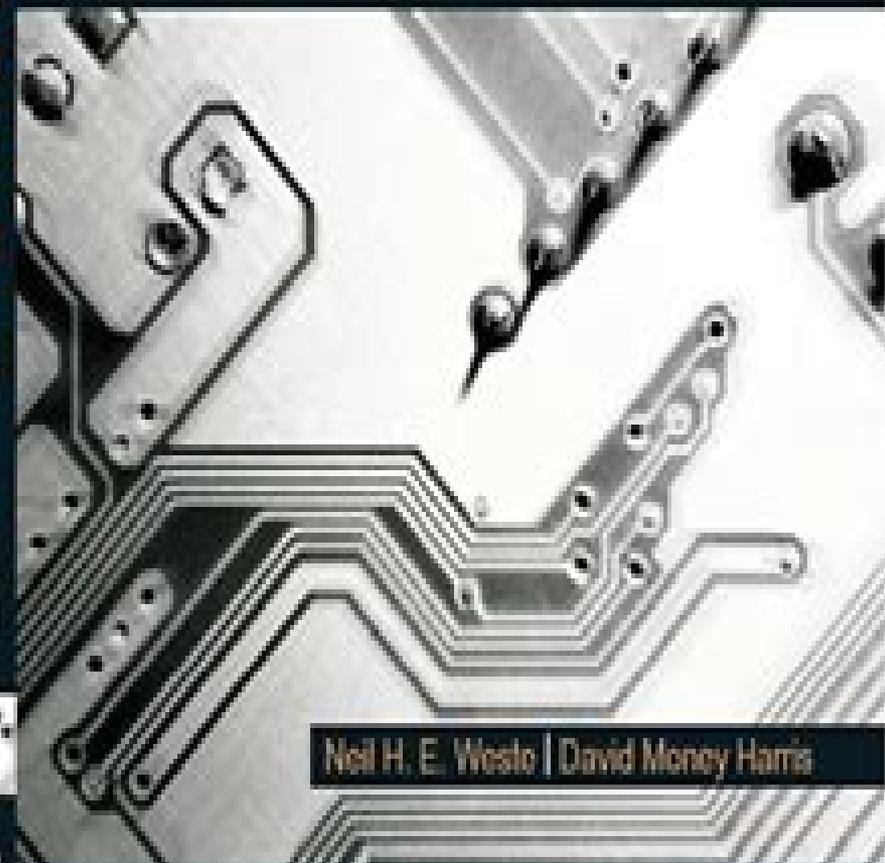


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Neil H. E. Weste | David Money Harris

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