

SECOND EDITION

# DIGITAL INTEGRATED CIRCUITS

A DESIGN PERSPECTIVE

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ANANTHA CHANDRAKASAN  
BORIVOJE NIKOLIĆ

Unique in approach, this text offers comprehensive coverage of new design methodologies and implementation strategies of digital ICs and brings to the forefront a number of new issues that impact the reliability, cost, performance, and power dissipation of chips now reaching the 100-nm realm. This means increased importance in circuit design of deep submicron transistor effects, interconnect, signal integrity, high-performance and low-power design, timing and clock distribution. In contrast to the first edition, the present text focuses entirely on CMOS ICs. While introducing these changes, the text preserves the spirit and goals of the first edition—that is, it successfully bridges the gap between the circuit and system visions on digital design.

Beginning with solid discussions on the operation of electronic devices and in-depth analysis of the nucleus of digital design—the inverter—the text gradually channels this knowledge into the design of more complex modules such as gates, controllers, adders, multipliers, and memories. Actual design examples and layouts are interspersed throughout the text. Besides numerous examples, each chapter also contains a number of problems or brain teasers that provoke thinking and understanding while reading. Fully worked-out design problems are available from the web site: (<http://bwrc.eecs.berkeley.edu/icBook>).

The text is intended for use in digital circuit design courses conducted for undergraduate and postgraduate students of electronics.



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# Digital Integrated Circuits A Design Perspective Solution Manual

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## **Digital Integrated Circuits A Design Perspective Solution Manual :**

Solution Manual to Accompany CMOS Digital Integrated Circuits : Analysis and Design, Second Edition Sung-Mo Kang, Yusuf Leblebici, 1999      **Introduction to VLSI Systems** Ming-Bo Lin, 2011-11-28 With the advance of semiconductors and ubiquitous computing the use of system on a chip SoC has become an essential technique to reduce product cost With this progress and continuous reduction of feature sizes and the development of very large scale integration VLSI circuits addressing the harder problems requires fundamental understanding of circuit and layout design issues Furthermore engineers can often develop their physical intuition to estimate the behavior of circuits rapidly without relying predominantly on computer aided design CAD tools Introduction to VLSI Systems A Logic Circuit and System Perspective addresses the need for teaching such a topic in terms of a logic circuit and system design perspective To achieve the above mentioned goals this classroom tested book focuses on Implementing a digital system as a full custom integrated circuit Switch logic design and useful paradigms that may apply to various static and dynamic logic families The fabrication and layout designs of complementary metal oxide semiconductor CMOS VLSI Important issues of modern CMOS processes including deep submicron devices circuit optimization interconnect modeling and optimization signal integrity power integrity clocking and timing power dissipation and electrostatic discharge ESD Introduction to VLSI Systems builds an understanding of integrated circuits from the bottom up paying much attention to logic circuit layout and system designs Armed with these tools readers can not only comprehensively understand the features and limitations of modern VLSI technologies but also have enough background to adapt to this ever changing field      **Solutions Manual to Accompany Analysis and Design of Digital Integrated Circuits** David A. Hodges, Yu Chen, Horace G. Jackson, 1983      *Embedded SoPC Design with Nios II Processor and VHDL Examples* Pong P. Chu, 2011-08-29 The book is divided into four major parts Part I covers HDL constructs and synthesis of basic digital circuits Part II provides an overview of embedded software development with the emphasis on low level I O access and drivers Part III demonstrates the design and development of hardware and software for several complex I O peripherals including PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides three case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology The book utilizes FPGA devices Nios II soft core processor and development platform from Altera Co which is one of the two main FPGA manufactures Altera has a generous university program that provides free software and discounted prototyping boards for educational institutions details at [www.altera.com/university](http://www.altera.com/university) The two main educational prototyping boards are known as DE1 99 and DE2 269 All experiments can be implemented and tested with these boards A board combined with this book becomes a turn key solution for the SoPC design experiments and projects Most HDL and C codes in the book are device independent and can be adapted by other prototyping boards as long as a board has similar I O

configuration     FPGA Prototyping by VHDL Examples Pong P. Chu,2011-09-20 This book uses a learn by doing approach to introduce the concepts and techniques of VHDL and FPGA to designers through a series of hands on experiments FPGA Prototyping by VHDL Examples provides a collection of clear easy to follow templates for quick code development a large number of practical examples to illustrate and reinforce the concepts and design techniques realistic projects that can be implemented and tested on a Xilinx prototyping board and a thorough exploration of the Xilinx PicoBlaze soft core microcontroller     *Embedded SoPC Design with Nios II Processor and Verilog Examples* Pong P. Chu,2012-05-14 Explores the unique hardware programmability of FPGA based embedded systems using a learn by doing approach to introduce the concepts and techniques for embedded SoPC design with Verilog An SoPC system on a programmable chip integrates a processor memory modules I O peripherals and custom hardware accelerators into a single FPGA field programmable gate array device In addition to the customized software customized hardware can be developed and incorporated into the embedded system as well allowing us to configure the soft core processor create tailored I O interfaces and develop specialized hardware accelerators for computation intensive tasks Utilizing an Altera FPGA prototyping board and its Nios II soft core processor *Embedded SoPC Design with Nios II Processor and Verilog Examples* takes a learn by doing approach to illustrate the hardware and software design and development process by including realistic projects that can be implemented and tested on the board Emphasizing hardware design and integration throughout the book is divided into four major parts Part I covers HDL and synthesis of custom hardware Part II introduces the Nios II processor and provides an overview of embedded software development Part III demonstrates the design and development of hardware and software of several complex I O peripherals including a PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides several case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology While designing and developing an embedded SoPC can be rewarding the learning can be a long and winding journey This book shows the trail ahead and guides readers through the initial steps to exploit the full potential of this emerging methodology     **Digital Integrated Circuits** Jan M. Rabaey,2003 Contents p pChapter 1 Introductionp pChapter 2 The Manufacturing Processp pChapter 3 The Devicesp pChapter 4 The Wirep pChapter 5 The CMOS Inverterp pChapter 6 Designing Combinational Logic Gates in CMOS     Solutions Manual for Digital Integrated Circuits Ayers John E,2003-09     **Subject Guide to Books in Print** ,1971     **Scientific and Technical Aerospace Reports** ,1983     **Integrated Circuit and System Design** ,2005     Computers, Control & Information Theory ,1985  
*15th Symposium on Integrated Circuits and Systems Design* Ricardo Augusto da Luz Reis,Ney Calazans,2002  
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