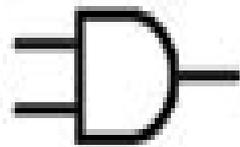
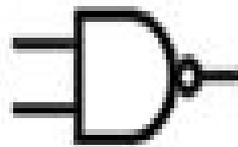


Basic Digital Logic Gates

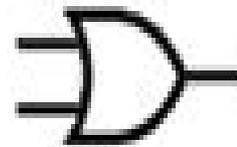
INPUT		OUTPUT
A	B	
0	0	0
1	0	0
0	1	0
1	1	1



AND



NAND



OR



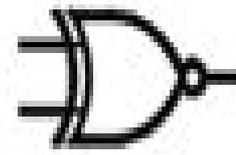
NOR



NOT



XOR



XNOR



A AND B	$A \cdot B$
A OR B	$A + B$
NOT A	\overline{A}
A XOR B	$A \oplus B$

Digital Logic Circuits

A.K. Singh



Digital Logic Circuits:

Digital Logic Circuits using VHDL Atul P. Godse, Dr. Deepali A. Godse, 2021-01-01 The book is written for an undergraduate course on digital electronics The book provides basic concepts procedures and several relevant examples to help the readers to understand the analysis and design of various digital circuits It also introduces hardware description language VHDL The book teaches you the logic gates logic families Boolean algebra simplification of logic functions analysis and design of combinational circuits using SSI and MSI circuits and analysis and design of the sequential circuits This book provides in depth information about multiplexers de multiplexers decoders encoders circuits for arithmetic operations various types of flip flops counters and registers It also covers asynchronous sequential circuits memories and programmable logic devices

Digital Electronics 1 Tertulien Ndjountche, 2016-06-20 The omnipresence of electronic devices in our everyday lives has been accompanied by the downscaling of chip feature sizes and the ever increasing complexity of digital circuits This book is devoted to the analysis and design of digital circuits where the signal can assume only two possible logic levels It deals with the basic principles and concepts of digital electronics It addresses all aspects of combinational logic and provides a detailed understanding of logic gates that are the basic components in the implementation of circuits used to perform functions and operations of Boolean algebra Combinational logic circuits are characterized by outputs that depend only on the actual input values Efficient techniques to derive logic equations are proposed together with methods of analysis and synthesis of combinational logic circuits Each chapter is well structured and is supplemented by a selection of solved exercises covering logic design practices

Fundamentals and Applications of Digital Logic Circuits Sol Libes, 1975

Digital Logic Techniques, 3rd Edition John Stonham, 1996-05-19 The third edition of Digital Logic Techniques provides a clear and comprehensive treatment of the representation of data operations on data combinational logic design sequential logic computer architecture and practical digital circuits A wealth of exercises and worked examples in each chapter give students valuable experience in applying the concepts and techniques discussed Beginning with an objective comparison between analogue and digital representation of data the author presents the Boolean algebra framework for digital electronics develops combinational logic design from first principles and presents cellular logic as an alternative structure more relevant than canonical forms to VLSI implementation He then addresses sequential logic design and develops a strategy for designing finite state machines giving students a solid foundation for more advanced studies in automata theory The second half of the book focuses on the digital system as an entity Here the author examines the implementation of logic systems in programmable hardware outlines the specification of a system explores arithmetic processors and elucidates fault diagnosis The final chapter examines the electrical properties of logic components compares the different logic families and highlights the problems that can arise in constructing practical hardware systems

Digital Logic Circuits Dr. P.

Kannan, Mrs. M. Saraswathi, Mr. C. Rameshkumar, PREFACE OF THE BOOK This book is extensively designed for the third

semester EEE EIE students as per Anna university syllabus R 2013 The following chapters constitute the following units
 Chapter 1 9 covers Unit 1 Chapter 2 and 3 covers Unit 2 Chapter 4 and 5 covers Unit 3 Chapter 6 and 7 covers Unit 4 Chapter
 8 VHDL Unit 5 CHAPTER 1 Introduces the Number System binary arithmetic and codes CHAPTER 2 Deals with Boolean
 algebra simplification using Boolean theorems K map method Quine McCluskey method logic gates implementation of
 switching function using basic Logical Gates and Universal Gates CHAPTER 3 Describes the combinational circuits like
 Adder Subtractor Multiplier Divider magnitude comparator encoder decoder code converters Multiplexer and Demultiplexer
 CHAPTER 4 Describes with Latches Flip Flops Registers and Counters CHAPTER 5 Concentrates on the Analysis as well as
 design of synchronous sequential circuits Design of synchronous counters sequence generator and Sequence detector
 CHAPTER 6 Concentrates the Design as well as Analysis of Fundamental Mode circuits Pulse mode Circuits Hazard Free
 Circuits ASM Chart and Design of Asynchronous counters CHAPTER 7 Discussion on memory devices which includes ROM
 RAM PLA PAL Sequential logic devices and ASIC CHAPTER 8 The chapter concentrates on the design fundamental building
 blocks Data types operates subprograms packages compilation process used for VHDL It discusses on Finite state machine
 as an important tool for designing logic level state machines The chapter also discusses register transform level designing
 and test benches usage in stimulation of the state logic machines CHAPTER 9 Concentrate on the comparison operation and
 characteristics of RTL DTL TTL ECL and MOS families We have taken enough care to present the definitions and statements
 of basic laws and theorems problems with simple steps to make the students familiar with the fundamentals of Digital Design

DIGITAL LOGIC DESIGN Sonali Singh, 2018-06-01 Description The book is an attempt to make Digital Logic Design
 easy and simple to understand The book covers various features of Logic Design using lots of examples and relevant
 diagrams The complete text is reviewed for its correctness This book is an outcome of sincere effort and hard work to bring
 concepts of Digital Logic Design close to the audience of this book The salient features of the book Easy explanation of
 Digital System and Binary Numbers with lots of solved examples Detailed covering of Boolean Algebra and Gate Level
 Minimization with proper examples and diagrammatic representation Detailed analysis of different Combinational Logic
 Circuits Complete Synchronous sequential Logic understanding Deep understanding of Memory and Programmable Logic
 Detailed analysis of different Asynchronous Sequential Logic Table Of Contents Unit 1 Digital System and Binary Numbers
 Part 1 Digital System and Binary Numbers Part 2 Boolean Algebra and Gate Level Minimization Unit 2 Combinational
 Logic Unit 3 Sequential Circuits Unit 4 Memory Programmable Logic and Design Unit 5 Asynchronous Sequential Logic

Digital Electronic Circuits - The Comprehensive View Alexander Axelevitch, 2018-09-25 This book deals with key
 aspects of design of digital electronic circuits for different families of elementary electronic devices Implementation of both
 simple and complex logic circuits are considered in detail with special attention paid to the design of digital systems based on
 complementary metal oxide semiconductor CMOS and Pass Transistor Logic PTL technologies acceptable for use in planar

microelectronics technology It is written for students in electronics and microelectronics with exercises and solutions provided Related Link s *Digital Logic Circuits (As Per Anna University)* A.K. Singh,2007 **Digital Circuits** Emery,1985-03-19 This textbook is intended to introduce the student of electronics to the fundamentals of digital circuits both combinational and sequential in a reasonable and systematic manner It proceeds from basic logic concepts to circuits and designs Fundamentals of Digital Logic with VHDL Design with CD-ROM Stephen Brown,Zvonko Vranesic,2008-04-14 Fundamentals of Digital Logic with VHDL Design teaches the basic design techniques for logic circuits The text provides a clear and easily understandable discussion of logic circuit design without the use of unnecessary formalism It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips Fundamental concepts are illustrated by using small examples which are easy to understand Then a modular approach is used to show how larger circuits are designed VHDL is a complex language so it is introduced gradually in the book Each VHDL feature is presented as it becomes pertinent for the circuits being discussed While it includes a discussion of VHDL the book provides thorough coverage of the fundamental concepts of logic circuit design independent of the use of VHDL and CAD tools A CD ROM containing all of the VHDL design examples used in the book as well Altera s Quartus II CAD software is included free with every text Digital Logic Circuit Analysis and Design Victor Nelson,Bill Carroll,H Nagle,David Irwin,2020-09-04 This print textbook is available for students to rent for their classes The Pearson print rental program provides students with affordable access to learning materials so they come to class ready to succeed Balance breadth and depth of coverage with practical real world design methods Digital Logic Circuit Analysis and Design provides an authoritative state of the art approach to the fundamentals of digital logic analysis and design that is highly supportive of student learning The book balances theory and practice in depth without getting bogged down in excessive technical or mathematical language Retaining its tradition of both clarity and rigor the 2nd Edition features extensive coverage of current topics of interest such as modeling with Verilog and VHDL design with programmable devices and computer aided design Filled with updated illustrations examples and problems this text helps students gain a solid sense of how theory underlies practice This title is also available digitally as a standalone Pearson eText Contact your Pearson rep for more information Digital Electronics 2 Tertulien Ndjountche,2016-08-16 As electronic devices become increasingly prevalent in everyday life digital circuits are becoming even more complex and smaller in size This book presents the basic principles of digital electronics in an accessible manner allowing the reader to grasp the principles of combinational and sequential logic and the underlying techniques for the analysis and design of digital circuits Providing a hands on approach this work introduces techniques and methods for establishing logic equations and designing and analyzing digital circuits Each chapter is supplemented with practical examples and well designed exercises with worked solutions This second of three volumes focuses on sequential and arithmetic logic circuits It covers various aspects related to the following topics latch and flip flop binary counters shift registers arithmetic and logic circuits digital integrated circuit

technology semiconductor memory programmable logic circuits Along with the two accompanying volumes this book is an indispensable tool for students at a bachelors or masters level seeking to improve their understanding of digital electronics and is detailed enough to serve as a reference for electronic automation and computer engineers *Digital Principles and Logic Design* Arijit Saha,Nilotpal Manna,2009-01-28 This text and reference provides students and practicing engineers with an introduction to the classical methods of designing electrical circuits but incorporates modern logic design techniques used in the latest microprocessors microcontrollers microcomputers and various LSI components The book provides a review of the classical methods e g the basic concepts of Boolean algebra combinational logic and sequential logic procedures before engaging in the practical design approach and the use of computer aided tools The book is enriched with numerous examples and their solutions over 500 illustrations and includes a CD ROM with simulations additional figures and third party software to illustrate the concepts discussed in the book *Digital Logic and Microprocessors* Frederick J. Hill,Gerald R. Peterson,1984 A carefully integrated treatment for a one or two semester first course in computer hardware at the sophomore junior level this text includes up to date discussions of digital logic combined with an in depth look at microprocessor programming and interface design An introduction to hardware description languages is provided as a means of describing more complex sequential circuits and as a transition to microprocessors *Digital Logic Circuits* Atul P. Godse,2007 *Fundamentals of Digital Logic with VHDL Design* Stephen Brown,Zvonko Vranesic,2008-04-11 Fundamentals of Digital Logic with VHDL Design teaches the basic design techniques for logic circuits It emphasises the synthesis of circuits and explains how circuits are implemented in real chips Fundamental concepts are illustrated by using small examples which are easy to understand **An Introduction to Logic Circuit Testing** Parag K. Lala,2022-06-01 An Introduction to Logic Circuit Testing provides a detailed coverage of techniques for test generation and testable design of digital electronic circuits systems The material covered in the book should be sufficient for a course or part of a course in digital circuit testing for senior level undergraduate and first year graduate students in Electrical Engineering and Computer Science The book will also be a valuable resource for engineers working in the industry This book has four chapters Chapter 1 deals with various types of faults that may occur in very large scale integration VLSI based digital circuits Chapter 2 introduces the major concepts of all test generation techniques such as redundancy fault coverage sensitization and backtracking Chapter 3 introduces the key concepts of testability followed by some ad hoc design for testability rules that can be used to enhance testability of combinational circuits Chapter 4 deals with test generation and response evaluation techniques used in BIST built in self test schemes for VLSI chips Table of Contents Introduction Fault Detection in Logic Circuits Design for Testability Built in Self Test References *Digital Logic Techniques* T. J. Stonham,1987 *Fundamentals of Digital Logic with Verilog Design* Stephen Brown,Zvonko Vranesic,2013-03-15 Fundamentals of Digital Logic With Verilog Design teaches the basic design techniques for logic circuits It emphasizes the synthesis of circuits and

explains how circuits are implemented in real chips Fundamental concepts are illustrated by using small examples Use of CAD software is well integrated into the book A CD ROM that contains Altera s Quartus CAD software comes free with every copy of the text The CAD software provides automatic mapping of a design written in Verilog into Field Programmable Gate Arrays FPGAs and Complex Programmable Logic Devices CPLDs Students will be able to try firsthand the book s Verilog examples over 140 and homework problems Engineers use Quartus CAD for designing simulating testing and implementing logic circuits The version included with this text supports all major features of the commercial product and comes with a compiler for the IEEE standard Verilog language Students will be able to enter a design into the CAD system compile the design into a selected device simulate the functionality and timing of the resulting circuit implement the designs in actual devices using the school s laboratory facilities Verilog is a complex language so it is introduced gradually in the book Each Verilog feature is presented as it becomes pertinent for the circuits being discussed To teach the student to use the Quartus CAD the book includes three tutorials

Foundations Of Digital Logic Design Abraham Kandel,Gideon Langholz,Joe L Mott,1998-08-11 This text is intended for a first course in digital logic design at the sophomore or junior level for electrical engineering computer engineering and computer science programs as well as for a number of other disciplines such as physics and mathematics The book can also be used for self study or for review by practicing engineers and computer scientists not intimately familiar with the subject After completing this text the student should be prepared for a second advanced course in digital design switching and automata theory microprocessors or computer organization

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