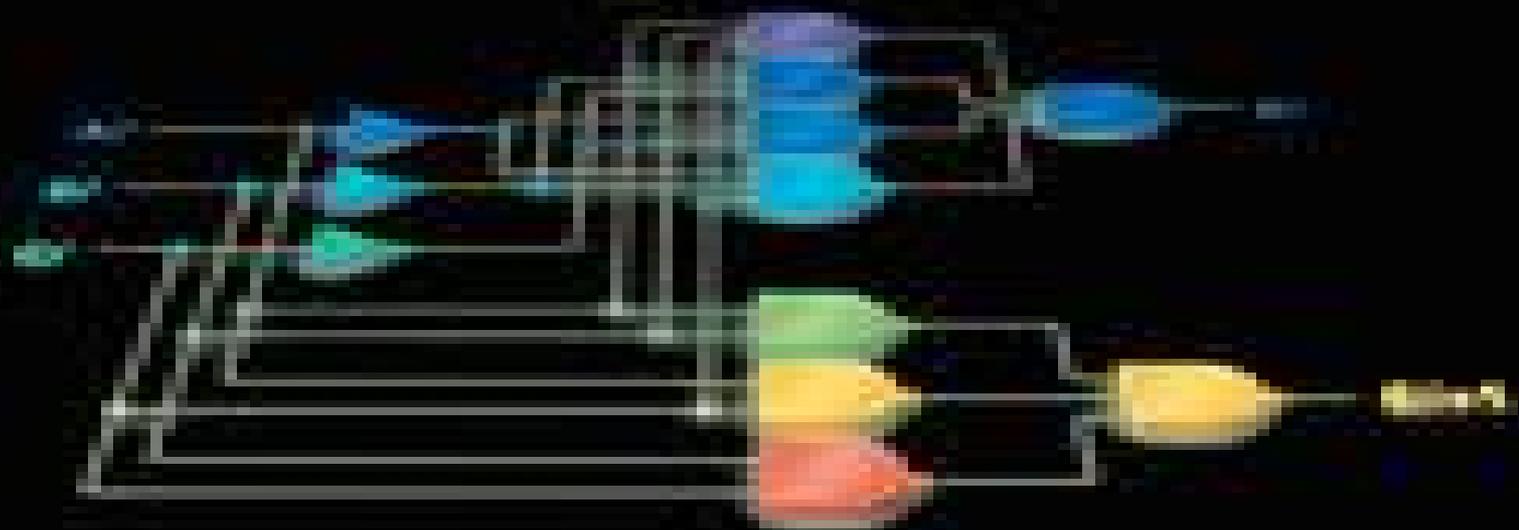


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Computer Logic

*Design Principles
and Applications*



Computer Logic Design 1st Edition

Huangqi Zhang



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Computer Organization and Design David A. Patterson,John L. Hennessy,2004-08-07 This best selling text on computer organization has been thoroughly updated to reflect the newest technologies Examples highlight the latest processor designs benchmarking standards languages and tools As with previous editions a MIPS processor is the core used to present the fundamentals of hardware technologies at work in a computer system The book presents an entire MIPS instruction set instruction by instruction the fundamentals of assembly language computer arithmetic pipelining memory hierarchies and I O A new aspect of the third edition is the explicit connection between program performance and CPU performance The authors show how hardware and software components such as the specific algorithm programming language compiler ISA and processor implementation impact program performance Throughout the book a new feature focusing on program performance describes how to search for bottlenecks and improve performance in various parts of the system The book digs deeper into the hardware software interface presenting a complete view of the function of the programming language and compiler crucial for understanding computer organization A CD provides a toolkit of simulators and compilers along with tutorials for using them For instructor resources click on the grey companion site button found on the right side of this page This new edition represents a major revision New to this edition Entire Text has been updated to reflect new technology 70% new exercises Includes a CD loaded with software projects and exercises to support courses using a number of tools A new interior design presents defined terms in the margin for quick reference A new feature Understanding Program Performance focuses on performance from the programmer's perspective Two sets of exercises and solutions For More Practice and In More Depth are included on the CD Check Yourself questions help students check their understanding of major concepts Computers In the Real World feature illustrates the diversity of uses for information technology More detail below

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Computer Science Oded Goldreich, Arnold L. Rosenberg, 2006-03-22 This volume commemorates Shimon Even one of founding fathers of Computer Science in Israel who passed away on May 1 2004 This Festschrift contains research contributions surveys and educational essays in theoretical computer science written by former students and close collaborators of Shimon The essays address natural computational problems and are accessible to most researchers in theoretical computer science

Introduction to Digital Logic Design John Patrick Hayes, 1993 Textbook

Fundamentals of Logic Design Charles H. Roth, 1985

Information Communication Technologies: Concepts, Methodologies, Tools, and Applications Van Slyke, Craig, 2008-04-30 The rapid development of information communication technologies ICTs is having a profound impact across numerous aspects of social economic and cultural activity worldwide and keeping pace with the associated effects implications opportunities and pitfalls has been challenging to researchers in diverse realms ranging from education to competitive intelligence

Starting Out with Programming Logic and Design Tony Gaddis, 2009-10-02 Starting Out with Programming Logic and Design is a language independent introductory programming book that orients students to programming concepts and logic without assuming any previous programming experience In the successful accessible style of Tony Gaddis best selling texts useful examples and detail oriented explanations allow students to become comfortable with fundamental concepts and logical thought processes used in programming without the complication of language syntax Students gain confidence in their program design skills to transition into more comprehensive programming courses

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Timed Boolean Functions William K.C. Lam, Robert K. Brayton, 2012-12-06 Timing research in high performance VLSI systems has advanced at a steady pace over the last few years while tools especially theoretical mechanisms lag behind Much present timing research relies heavily on timing diagrams which although intuitive are inadequate for analysis of large designs with many parameters Further timing diagrams offer only approximations not exact solutions to many timing problems and provide little insight in the cases where temporal properties of a design interact intricately with the design's logical functionalities This book presents a methodology for timing research which facilitates analysis and design of circuits and systems in a unified temporal and logical domain In the first part we introduce an algebraic representation formalism Timed Boolean Functions TBFs which integrates both logical and timing information of digital circuits and systems into a single formalism We also give a canonical form TBF BDDs for them which can be used for efficient manipulation In the second part we apply Timed Boolean Functions to three problems in timing research for which exact solutions are obtained for the first time 1 computing the exact delays of combinational circuits and the minimum cycle times of finite state machines 2 analysis and synthesis of wavepipelining

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