

# Tanner EDA Releases T-Spice

Pasadena, Calif.--Feb. 23, 1998-- Tanner EDA (Pasadena) announced the V5 release of the T-Spice Pro integrated circuit design capture and simulation system for Windows 95 and NT. The tool suite includes the S-Edit schematic editor, the T-Spice circuit simulator, and the W-Edit waveform viewer. The new release includes improvements in user interface, integration, and simulation and modeling technology.

Tanner's hierarchic schematic capture tool, S-Edit, has been overhauled to add many new Windows 95 and NT user interface improvements. Other enhancements include a superior simulation interface and more flexible design export capabilities. A new browser gives quick access to cell library components. Library files can be easily added, removed, or accessed for design reuse. A "quick pick" feature gives fast access to frequently used components.

S-Edit is well integrated with the T-Spice circuit simulator and W-Edit waveform viewer. Simulation results can be viewed by probing the schematic using the new waveform probing feature. New design export features include support for parametric subcircuits, control of subcircuit pin orders, and output in EDIF 2.0.0 and VHDL formats. Compatibility with L-Edit simplifies the design process--there is no longer a need to use mismatched design capture, simulation, and layout tools.

Tanner's circuit simulator, T-Spice, contains the BSIM3v3.1 MOSFET model, the latest version of Berkeley's BSIM3 model. New MESFET models have also been added for GaAs IC support. T-Spice accepts the latest parasitic and size-based device characterization data available from foundries to give accurate simulation results.

For devices that cannot be represented by one of T-Spice's internal models, custom models can be written using a new external model interface. Written in the C programming language, external models can be simulated using a built-in C-interpreter, or compiled into a dynamically linked library (DLL). The general purpose interface allows the addition of functional and behavioral level models in addition to custom transistor models.

T-Spice's new enhanced Fourier analysis command contains new accuracy and output control options. A waveform relaxation engine has been added as an alternative to the standard transient analysis method for simulation of large circuits. Simulation queuing is also supported for running multiple T-Spice analyses. Several new commands and options have been added to control simulation methods, output data compression, case sensitivity, device scaling, output expression handling, and simulation statistics. In addition, T-Spice's netlist manipulation tools have been enhanced with undo and redo, and regular expression search and replace capabilities.

Additions to W-Edit improve the power and flexibility for graphically viewing simulation output data. New controls provide precise adjustment of the graphical display of output data. A waveform arithmetic tool can display traces formed by writing mathematical expressions. Traces defined by expressions can be composed of arithmetic combinations of other trace names or can

# T Spice Pro Circuit Analysis Tutorial

**Rosina Ehmann**



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*IEEE Circuits & Devices* ,1995    *ASIC & EDA* ,1994    *EDN.* ,1986    **Computer-Aided Design of Analog Integrated Circuits and Systems** Rob A. Rutenbar, Georges G. E. Gielen, 2002-05-06 The tools and techniques you need to break the analog design bottleneck Ten years ago analog seemed to be a dead end technology Today System on Chip SoC designs are increasingly mixed signal designs With the advent of application specific integrated circuits ASIC technologies that can integrate both analog and digital functions on a single chip analog has become more crucial than ever to the design process Today designers are moving beyond hand crafted one transistor at a time methods They are using new circuit and physical synthesis tools to design practical analog circuits new modeling and analysis tools to allow rapid exploration of system level alternatives and new simulation tools to provide accurate answers for analog circuit behaviors and interactions that were considered impossible to handle only a few years ago To give circuit designers and CAD professionals a better understanding of the history and the current state of the art in the field this volume collects in one place the essential set of analog CAD papers that form the foundation of today s new analog design automation tools Areas covered are Analog synthesis Symbolic analysis Analog layout Analog modeling and analysis Specialized analog simulation Circuit centering and yield optimization Circuit testing Computer Aided Design of Analog Integrated Circuits and Systems is the cutting edge reference that will be an invaluable resource for every semiconductor circuit designer and CAD professional who hopes to break the analog design bottleneck    **Signal Integrity Effects in Custom IC and ASIC Designs** Raminderpal Singh, 2001-12-12 offers a tutorial guide to IC designers who want to move to the next level of chip design by unlocking the secrets of signal integrity Jake Buurma Senior Vice President Worldwide Research Development Cadence Design Systems Inc Covers signal integrity effects in high performance Radio Frequency RF IC Brings together research papers from the past few years that address the broad range of issues faced by IC designers and CAD managers now and in the future A Wiley IEEE Press publication    *VLSI Systems Design* ,1985    **Proceedings of the ... Midwest Symposium on Circuits and Systems** ,1992    **Electronic Design** ,1989    **Proceedings of the 34th Midwest Symposium on Circuits and Systems** Sherif Michael, 1991    **Introduction to Device Modeling and Circuit Simulation** Tor A. Fjeldly, Trond Ytterdal, Michael S. Shur, 1998 This book is a useful reference for practicing electrical engineers as well as a textbook for a junior senior or graduate level course in electrical engineering The authors combine two subjects device modeling and circuit simulation by providing a large number of well prepared examples of circuit simulations immediately following the description of many device models    **Towards 2000** Lawrence P. Grayson, 1992    *Radio Communication Handbook* Mike Dennison, 2007    **VLSI Design** ,1983    *Byte* ,1992    **Electronics Now** ,1996    **General Catalog -- University of California, Santa Cruz** University of California, Santa Cruz, 2001    *Exhibitors Daily Review* ,1926    *Atlanta* ,2004-02 Atlanta magazine s editorial mission is to engage our community through provocative writing authoritative reporting and

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