Q.5   Divide overflow is generated when
       (A) Sign of the dividend is different from that of divisor.
       (B) Sign of the dividend is same as that of divisor.
       (C) The first part of the dividend is smaller than the divisor.
       (D) The first part of the dividend is greater than the divisor.

       Ans: B
       If the first part of the dividend is greater than the deviser, then the result should be
       of greater length, then that can be hold in a register of the system. The registers are
       of fixed length in
       any processor.

Q.6   Which method is used for resolving data dependency conflict by the compiler
       itself?
       (A) Delayed load.   (B) operand forwarding.
       (C) Pre fetch target instruction.   (D) loop buffer.

       Ans: A
       In case of delayed load technique the complier detects the data conflict and reorder
       the instruction as necessary to delay the loading of the conflicting data by inserting
       no operation instructions.

Q.7   Stack overflow causes
       (A) Hardware interrupt.
       (B) External interrupt.
       (C) Internal interrupt.
       (D) Software interrupt.

       Ans: C
       Stack overflow occurs while execution of a program due to logical faults. So it is a
       program dependent, hence interrupt activated.

Q.8   Arithmetic shift left operation
       (A) Produces the same result as obtained with logical shift left operation.
       (B) Causes the sign bit to remain always unchanged.
       (C) Needs additional hardware to preserve the sign bit.
       (D) Is not applicable for signed 2's complement representation.
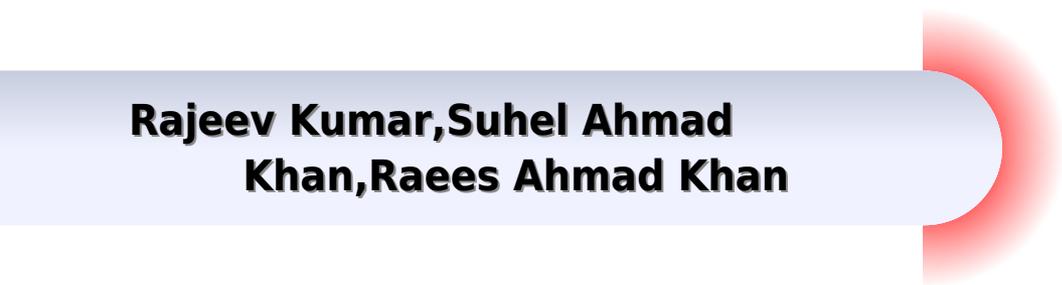
       Ans: A
       If the register hold minus five in two's compliment form than in arithmetic shift left
       the contents of the register shall be

       Initial Register content   After Shift Left
       | 1 | 1 | 0 | 1 | 1 | | 1 | 0 | 1 | 0 | 1 |
       = (-5) in 2's Compliment form   = (-10) in 2's Compliment form

       It is found that the register contents multiplied by two after logical shift left
       operation. Hence arithmetic shift left operation is same as logical shift operation.

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