

CADENCE ANALOG/ MIXED-SIGNAL DESIGN METHODOLOGY

The Cadence® Analog/Mixed-Signal (AMS) Design Methodology employs advanced Cadence Virtuoso® custom design technologies and leverages silicon-accurate design flows to help design teams create differentiated silicon faster and with less risk. It delivers verified and packaged methodologies demonstrated on a real-world mixed-signal design. The Cadence AMS Design Methodology combines the best of top-down (behavioral and mixed-level approaches) with bottom-up (transistor-level design and abstraction) design techniques to achieve predictable, high-quality results for complex mixed-signal designs.

AMS DESIGN METHODOLOGY

The Cadence AMS Design Methodology delivers an extensive design and data flow guide, from design specification through design manufacturing, across the different functions of a design team. It is based on executable design tasks and recommended use models for fast, silicon-accurate mixed-signal design that ensures first-pass silicon success. The AMS Design Methodology addresses the analog-driven mixed-signal design process front to back by executing well-defined flows that demonstrate a meet-in-the-middle approach, in which all design flows are running in parallel to minimize design iterations, maximize project resource utilization, and enhance design quality.

The AMS Design Methodology addresses the entire design process and comprises five major flows:

1. Design environment and infrastructure
2. Top-down functional verification
3. AMS IP block creation and reuse
4. AMS IP export and integration
5. Top-down physical design



Figure 1: The Cadence AMS Design Methodology consists of five major flows.

Cadence Analog Mixed Signal Design Methodology

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