

# General Purpose Transistors

## NPN Silicon

### FEATURES

- High current capacity in compact package
- 50, 100, 200
- Epitaxial power type
- JEDEC compliance: TO18
- Pin-Trim Package is available
- ICL: Suitable for Automotive and Other Applications Requiring Integer Size and Control Change Requirements: AEC-Q101 Qualified and PFMT Capable

### DEVICE MARKING AND ORDERING INFORMATION

Device	Marking	Shipping
L8050H01TIG	S-L8050H01TIG	1000Tape and Reel
L8050H02TIG	S-L8050H02TIG	1000Tape and Reel
L8050H03TIG	S-L8050H03TIG	1000Tape and Reel
L8050H04TIG	S-L8050H04TIG	1000Tape and Reel
L8050H05TIG	S-L8050H05TIG	1000Tape and Reel
L8050H06TIG	S-L8050H06TIG	1000Tape and Reel
L8050H07TIG	S-L8050H07TIG	1000Tape and Reel
L8050H08TIG	S-L8050H08TIG	1000Tape and Reel

L8050H01TIG Series  
S-L8050H01TIG Series



### MAXIMUM RATINGS

Rating	Symbol	Min	Max
Collector-Emitter Voltage	$V_{CE}$	25	V
Collector-Base Voltage	$V_{CB}$	40	V
Collector-Base Voltage	$V_{EB}$	5	V
Collector Current	$I_C$	1000	mA

### TERMINAL CHARACTERISTICS

Characterization	Symbol	Min	Max
Total Device Dissipation (TA = 25°C)	$P_D$	200	mW
TA = 25°C Derate above 25°C		0.8	mW/°C
Thermal Resistance, Junction to Ambient	$\theta_{JA}$	500	°C/W
Total Device Dissipation (Ambient Temperature = 25°C)	$P_D$	800	mW
Average Dissipation (TA = 25°C) Derate above 25°C		0.8	mW/°C
Thermal Resistance, Junction to Ambient Ambient and Device Temperature	$\theta_{JA}$	411	°C/W
	$\theta_{JC}$	55 to 450	°C

1.  $P_D$  is in mW and  $P_{D(AVG)}$  is in mW.

2. Ambient = 25°C and  $\theta_{JA}$  = 0.0264 in. sq. in. square.

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web a situation in which a person who belongs to a particular group is believed by other people to be a member of a different group for example a different race reasons for passing are

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