

EMC and System-ESD Design Guidelines for Board Layout

About this document

Scope and purpose

This document provides information for EMC optimized PCB design and system ESD design. The topics covered include PCB Design considerations regarding the routing of high speed signals, selecting stack-up of the PCB, selecting decoupling components, impedance controlled design of the traces, and termination of high speed signal paths. Special considerations for microcontrollers are also provided.

Attention: *This application note contains design recommendations from Infineon Technologies point of view. Effectiveness and performance of the final application implementation must be validated by the customer, based on their specific implementation choices.*

Intended audience

This document is intended for anyone who needs to design EMC optimized application boards using microcontrollers and other components from Infineon Technologies AG.

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JA Banks



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