8086 INSTRUCTION SET

OPCODE		DESCRIPTION
AAA		ASCII adjust addition
AAD		ASCII adjust division
AAM		ASCII adjust multiply
AAS		ASCII adjust subtraction
ADC	d,s	ADD with carry
ADD	d,s	ADD
AND	d,s	Logical AND
CALL	proc	Call a procedure
CBW		Convert byte to word
CLC		Clear carry flag
CLD		Clear direction flag
CLI		Clear interrupt flag
CMC		Complement carry flag
CMP	d,s	Compare
CMPS	d,s	Compare string
CMPSB	"	" bytes
CMPSW	"	" words
CWD		Convert word to double word
DAA		Decimal adjust addition
DAS		Decimal adjust subtraction
DEC	d	Decrement
DIV	s	Unsigned divide
ESC	code,s	Escape
HLT		Halt
IDIV	s	Integer divide
IMUL	s	Integer multiply
IN	d,port	Input from port
INC	d	Increment
INT	type	Interrupt
INTO		Interrupt if overflow
IRET		Return from interrupt
JA	slabel	Jump if above
JAE	slabel	Jump if above or equal
JB	slabel	Jump if below
JBE	slabel	Jump if below or equal
JC	slabel	Jump if carry
JCXZ	slabel	Jump if CX is zero
JE	slabel	Jump if equal
JG	slabel	Jump if greater
JGE	slabel	Jump if greater or equal
JL	slabel	Jump if less
JLE	slabel	Jump if less or equal
JMP	label	Jump
JNA	slabel	Jump if non above
JNAE	slabel	Jump if not above or equal
JNB	slabel	Jump if not below
JNBE	slabel	Jump if below or equal
JNC	slabel	Jump if no carry
JNE	slabel	Jump if not equal
JNG	slabel	Jump if not greater
JNGE	slabel	Jump if not greater or equal
JNL	slabel	Jump if not less
JNLE	slabel	Jump if not less or equal
JNO	slabel	Jump if not overflow
JNP	slabel	Jump if not parity
JNS	slabel	Jump if not sign
JO	slabel	Jump if overflow
JP	slabel	Jump if parity odd
JS	slabel	Jump if parity
JPE	slabel	Jump if parity even
JS	slabel	Jump if sign
JZ	slabel	Jump if zero
LAHF		Load AH from flags
LDS	d,s	Load pointer using DS
LEA	d,s	Load effective address
LES	d,s	Load pointer using ES
LOCK		Lock bus
LODS	[src]	Load string
LODSB	"	" bytes
LODSW	"	" words
LOOP	slabel	Loop
LOOPE	slabel	Loop if equal
LOOPZ	slabel	Loop if zero
LOOPNE	slabel	Loop if not equal
LOOPNZ	slabel	Loop if not zero
MOV	d,s	Move
MOVS	[d,s]	Move string
MOVSB	"	" bytes
MOVSW	"	" words
MUL	s	Unsigned multiply
NEG	d	Negate
NOP		No operation
NOT	d	Logical NOT
OR	d,s	Logical OR
OUT	port,s	Output to port
POP	d	Pop word off stack
POPF		Pop flags off stack
PUSH	s	Push word onto stack
PUSHF		Push flags onto stack
RCL	d,cnt	Rotate left through carry
RCR	d,cnt	Rotate right through carry
REP		Repeat string operation
REPE		Repeat while equal
REPZ		Repeat while zero
REPNE		Repeat while not equal
REPNZ		Repeat while not zero
RET	[pop]	Return from procedure
ROL	d,cnt	Rotate left
ROR	d,cnt	Rotate right
SAHF		Store AH into flags
SAL	d,cnt	Shift arithmetic left
SHL	d,cnt	Shift logical left
SAR	d,cnt	Shift arithmetic right
SBB	d,s	Subtract with borrow
SCAS	d,s	Scan string
SCASB	"	" byte
SCASW	"	" word
SHR	d,cnt	Shift logical right
STC		Set carry flag
STD		Set direction flag
STI		Set interrupt flag
STOS	[d]	Store string
STOSB	"	" byte
STOSW	"	" word
SUB	d,s	Subtraction
TEST	d,s	Test (logical AND)
WAIT		Wait for 8087
XCHG	d,s	Exchange
XLAT	table	Translate
XLATB	"	"
XOR	d,s	Logical exclusive OR

Notes:
d = destination
s = source
label = May be near or far address
slabel = near address

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Stephen P. Morse

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8086/8088 User's Manual Intel Corporation,1989 **Microprocessor 8086 : Architecture, Programming and Interfacing** Mathur Sunil,2010-12 Primarily intended for the undergraduate students of electronics and communication engineering computer science and engineering and information technology this book skilfully integrates both the hardware and software aspects of the 8086 microprocessor It offers the students an up to date account of the state of the art microprocessors and therefore can be regarded as an incomparable source of information on recently developed microprocessor chips The book covers the advanced microprocessor architecture of the Intel microprocessor family from 8086 to Pentium 4 The text is organized in four parts Part I Chapters 1 7 includes a detailed description of the architecture organization instruction set and assembler directives of microprocessor 8086 Part II Chapters 8 11 discusses the math coprocessor multiprocessing and multiprogramming the different types of data transfer schemes and memory concepts Part III Chapters 12 15 covers programmable interfacing chips with the help of extensive interfacing examples Part IV Chapters 16 18 deals with advanced processors from 80186 to Pentium 4 This well organized and student friendly text should prone to be an invaluable asset to the students as well as the practising engineers KEY FEATURES Gives elaborate programming examples to develop the analytical ability of students Provides solved examples covering different types of typical interfacing problems to develop the practical skills of students Furnishes chapter end exercises to reinforce the understanding of the subject *Programming the Intel 80386* Bud E. Smith,Mark T. Johnson,1987 *The 8086 Primer* Stephen P. Morse,1980 Microprocessor 8085, 8086 Abhishek Yadav,2008 *The Intel Microprocessors* Barry B. Brey,2006 KEY BENEFIT Updated and current this book provides a comprehensive view of programming and interfacing of the Intel family of microprocessors from the 8088 through the latest Pentium 4 microprocessor KEY TOPICS Organized in an orderly and manageable format it offers over 200 programming examples using the Microsoft Macro Assembler program and provides a thorough description of each Intel family members memory systems and various I O systems MARKET For Electronic engineering specialist programmers computer scientists or electrical engineers **The Intel Microprocessors** Barry B. Brey,1991 *The 8086/8088 Family* John E. Uffenbeck,1987 This book presents the full range of Intel 80x86 microprocessors in context as a component of a comprehensive microprocessor system It provides a thorough single volume coverage of all Intel processors relative to their application in the PC and is as much an introduction to the PC itself as to Intel chips Covers all PC related technologies including memory data communications and PC bus standards The second edition of The 8086 8088 Family Design Programming and Interfacing has been revised to include the latest most up to date information and technologies This edition now covers Windows a description of the MS DOS BIOS services and function calls two completely revised software chapters an updated chapter on memory coverage of the 16550 UART and common modern standards and a new chapter on PC architecture and the common bus systems **Introduction to Computer Engineering**

Richard E. Haskell,1993 This text is for first and second year undergraduates studying the fundamentals of computer engineering digital logic and microprocessors Assuming little background in computer systems the book presents the basics then illustrates them with and examination of 8086 architecture and programming The intention is to teach digital logic by using programmable logic devices PLDs and the CUPL language

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