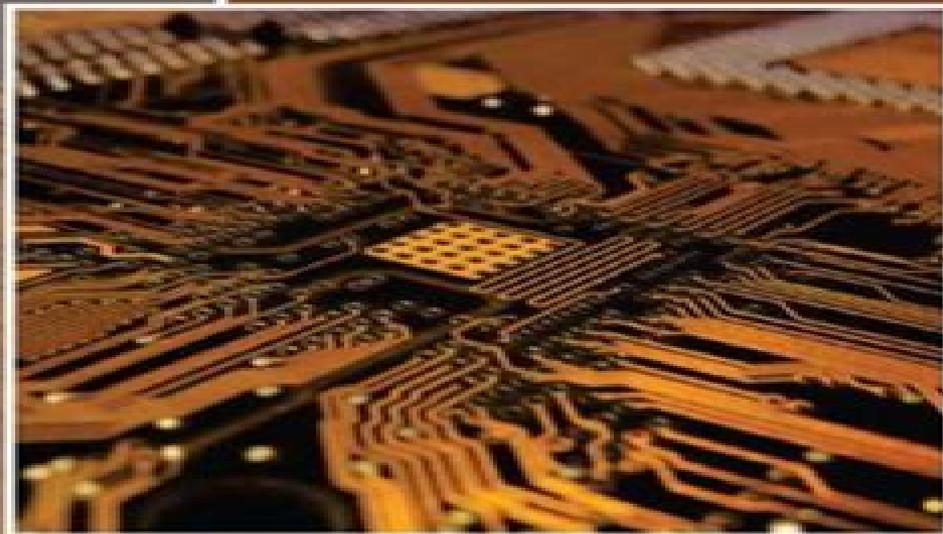


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*with Computer-Based Exercises
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Robert F. Pierret

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**IEEE Electron Devices
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Ashraf,2025-03-26 Parameters that determine the performance of silicon based Field Effect Transistors FET devices in the presence of degenerate doping often are not modeled properly and so require precise analysis to improve modeling accuracy The book is focused on the extraction of parameters for silicon based FET models that critically determine the FET performance at room temperature as well as at very low temperatures Emphasize is put on analysis that is based on the device physics especially at low cryogenic temperatures Performance of gate all around GAA nanowire FETs and stacked nanosheet complementary FETs C FET are also discussed **Essential Semiconductor Physics** Mark S

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 - Chapter 5: Conclusion
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The book is crafted in an easy-to-understand language and is complemented by engaging illustrations. It is highly recommended for anyone seeking to gain a comprehensive understanding of Semiconductor Device Fundamentals By Robert F Pierret.

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