

# ia-64 linux kernel

design and implementation

david mosberger • stéphane eranian



Authoritative and complete description of the Linux kernel  
for IA-64/ITP (Itanium Processor Family)

Provides an in-depth discussion of the ideas and concepts behind the Linux hardware  
abstraction interface, the IA-64 design space, and their actual realizations

From the lead architects and developers of the IA-64 Linux kernel

Foreword by Bruce Perens



# Ia 64 Linux Kernel Design And Implementation

**Albert A Gayle**



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*IA-64 Linux Kernel* David Mosberger, Stéphane Eranian, 2002 The IA 64 Linux kernel makes extraordinary power available to every Linux developer In IA 64 Linux Kernel Design and Implementation the kernel project s leaders systematically present every major subsystem introducing interfaces used by Linux to abstract platform differences showing how these interfaces are used in IA 64 and illuminating key issues associated with Linux kernel operation on any platform Covers processes tasks threads virtual memory I O symmetric multiprocessing bootstrapping and more [IA-64 LINUX KERNEL](#) (□□ □□□) DAVID MOSBERGER□, 2003-07-30

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[Itanium Architecture for Programmers](#) James S. Evans, Gregory L. Trimper, 2003 Step by step guide to assembly language for the 64 bit Itanium processors with extensive examples Details of Explicitly Parallel Instruction Computing EPIC Instruction set addressing register stack engine predication I O procedure calls floating point operations and more Learn how to comprehend and optimize open source Intel and HP UX compiler output Understand the full power of 64 bit Itanium EPIC processors Itaniumreg Architecture for Programmers is a comprehensive introduction to the breakthrough capabilities of the new 64 bit Itanium architecture Using standard command line tools and extensive examples the authors illuminate the Itanium design within the broader context of contemporary computer architecture via a step by step investigation of Itanium assembly language Coverage includes The potential of Explicitly Parallel Instruction Computing EPIC Itanium instruction formats and addressing modes Innovations such as the register stack engine RSE and extensive predication Procedure calls and procedure calling mechanisms Floating point operations I O techniques from

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**Journal of Computer Resource Management**, 2006 *2004 International Conference on Supercomputing*, 2004

**Sys Admin**, 2002

**Operating Systems** William Stallings, 2009 For a one semester undergraduate course in operating systems for computer science computer engineering and electrical engineering majors Winner of the 2009 Textbook Excellence Award from the Text and Academic Authors Association TAA Operating Systems Internals and Design Principles is a comprehensive and unified introduction to operating systems By using several innovative tools Stallings makes it possible to understand critical core concepts that can be fundamentally challenging The new edition includes the implementation of web based animations to aid visual learners At key points in the book students are directed to view an animation and then are provided with assignments to alter the animation input and analyze the results The concepts are then enhanced and supported by end of chapter case studies of UNIX Linux and Windows Vista These provide students with a solid understanding of the key mechanisms of modern operating systems and the types of design tradeoffs and decisions involved in OS design Because they are embedded into the text as end of chapter material students are able to apply them right at the point of discussion This approach is equally useful as a basic reference and as an up to date survey of the state of the art

[The Linux Networking Architecture](#) Klaus Wehrle, 2004 This unique Linux

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