Advanced FPGA Design: Architecture. implementation. and optimization(Chinese Edition)

(MEI)Steve Kilts MENG XIAN YUAN

Advanced Fpga Design Architecture Implementation And Optimization

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Advanced Fpga Design Architecture Implementation And Optimization:

Advanced FPGA Design Steve Kilts,2007-07-13 This book provides the advanced issues of FPGA design as the underlying theme of the work In practice an engineer typically needs to be mentored for several years before these principles are appropriately utilized The topics that will be discussed in this book are essential to designing FPGA s beyond moderate complexity The goal of the book is to present practical design techniques that are otherwise only available through mentorship and real world experience *Reconfigurable Computing: Architectures, Tools and Applications* Phaophak Sirisuk,Fearghal Morgan,Tarek El-Ghazawi,Hideharu Amano,2010-03-17 This book constitutes the proceedings of the 6th International Symposium on Reconfigurable Computing Architectures Tools and Applications ARC 2010 held in Bangkok Thailand in March 2010 The 42 papers presented consisting of 26 full and 16 short papers were carefully reviewed and selected from numerous submissions The topics covered are practical applications of the RC technology RC architectures TC design methodologies and tools and RC education Logic Synthesis for VLSI-Based Combined Finite State Machines Alexander Barkalov,Larysa Titarenko,Kamil Mielcarek,Małgorzata Mazurkiewicz,Elżbieta Kawecka,2022-11-24 The book is devoted to design and optimization of control units represented by combined finite state machines CFSMs The CFSMs combine features of both Mealy and Moore FSMs Having states of Moore FSM they produce output signals of both Mealy and Moore types To optimize the circuits of CFSMs we propose to use optimization methods targeting both Mealy and Moore FSMs The book contains some original synthesis and optimization methods targeting hardware reduction in VLSI based CFSM circuits These methods take into account the peculiarities of both a CFSM model and a VLSI chip in use The optimization is achieved due to combining classical optimization methods with new methods proposed in this book These new methods are a mixed encoding of collections of microoperations and a twofold state assignment in CFSMs All proposed methods target reducing the numbers of arguments in systems of Boolean functions representing CFSM circuits Also we propose to use classes of pseudoequivalent states of Moore FSMs to reduce the number of product terms in these systems The book includes a lot of examples which contributes to a better understanding of the features of the synthesis methods under consideration This is the first book entirely devoted to the problems associated with synthesis and optimization of VLSI based CFSMs We hope that the book will be interesting and useful for students and PhD students in the area of Computer Science as well as for designers of various digital systems We think that proposed CFSM models enlarge the class of models applied for implementation of control units with modern VLSI chips **Timing Performance of Nanometer Digital Circuits Under Process Variations** Victor Champac,Jose Garcia Gervacio,2018-04-18 This book discusses the digital design of integrated circuits under process variations with a focus on design time solutions The authors describe a step by step methodology going from logic gates to logic paths to the circuit level Topics are presented in comprehensively without overwhelming use of analytical formulations Emphasis is placed on providing digital designers with understanding of the

sources of process variations their impact on circuit performance and tools for improving their designs to comply with product specifications Various circuit level design hints are highlighted so that readers can use then to improve their designs A special treatment is devoted to unique design issues and the impact of process variations on the performance of FinFET based circuits This book enables readers to make optimal decisions at design time toward more efficient circuits with better yield and higher reliability **Bioinformatics** Horacio Pérez-Sánchez,2012-11-28 This book is divided into different research areas relevant in Bioinformatics such as biological networks next generation sequencing high performance computing molecular modeling structural bioinformatics molecular modeling and intelligent data analysis Each book section introduces the basic concepts and then explains its application to problems of great relevance so both novice and expert readers can benefit from the information and research works presented here Advances in Soft Computing Grigori Sidorov,Arturo Hernández Aguirre,Carlos Alberto Reyes Garcia,2010-10-31 Artificial intelligence AI is a branch of computer science that models the human ability of reasoning usage of human language and organization of knowledge solving problems and practically all other human intellectual abilities Usually it is characterized by the application of heuristic methods because in the majority of cases there is no exact solution to this kind of problem Soft computing can be viewed as a branch of AI that deals with the problems that explicitly contain incomplete or complex information or are known to be impossible for direct computation i e these are the same problems as in AI but viewed from the perspective of their computation The Mexican International Conference on Artificial Intelligence MICAI a yearly international conference series organized by the Mexican Society for Artificial Intelligence SMIA is a major international AI forum and the main event in the academic life of the country s growing AI community In 2010 SMIA celebrated 10 years of activity related to the organization of MICAI as is represented in its slogan Ten years on the road with AI MICAI conferences traditionally publish high quality papers in all areas of artificial intelligence and its applications The proceedings of the previous MICAI events were also published by Springer in its Lecture Notes in Artificial Intelligence LNAI series vols 1793 2313 2972 3789 4293 4827 5317 and 5845 Since its foundation in 2000 the conference has been growing in popularity and improving in quality **The British National Bibliography** Arthur James Wells,2007 **FPGA-based Implementation of Signal Processing Systems** Roger Woods,2008-11-24 The automation of the techniques for component architectural synthesis computational models and the reduction of energy consumption to help improve FPGA performance are given in detail FPGA based Implementation of Signal Processing Systems is an important reference for practising engineers and researchers working on the design and development of DSP systems for radio telecommunication information audio visual and security applications Senior level electrical and computer engineering graduates taking courses in signal processing or digital signal processing shall also find this volume of interest BOOK JACKET Courses and Programs Iowa State University,1999 **Undergraduate and Graduate Courses and Programs** Iowa State University,2009 **FPGA ...** ,2007

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