

Challenge Tutorial 01: Using Design Variables and Parametric Analysis

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Introduction

Design variables provide a method for specifying critical parameters just before a simulation. This allows you to make quick changes to parameter values across single or multiple critical components without having to edit any schematic components. Design variables can also be used to create parametric analysis, which are groups of design variables across a range of values and display multiple simulation results on a single plot.

Using Design Variables

This part of the tutorial will show you how to parametrize the width of the PCBs and their placement of an antenna by using design variables. These variable values are then set using the **Running Through Environment** panel in simulation.

1. Make a copy of the circuitry with components you have previously designed, or create a new one.
2. Place the components of the circuitry.
3. Edit each one of the PCBs to include an output or, and name the properties by using clicking on it and entering **Proprietor**. For you can give "y" as identifier.
4. Define the size of the width from $200\mu m$ to $300\mu m$. This specifies that a variable called **width** will be used to set the width of the PCBs. When you do this, observe that system for "Thermal/Electrical Difference Keys" change from a constant value to a formula that involves the variable **width**. Always check to make sure that formula is updated when you change the width to a variable otherwise, your simulation results will be incorrect. Click **OK** to apply the change.
5. Repeat this procedure to change the width of the PCBs to **width**.
6. Save **Change** \rightarrow **Check and Save**. Verify that there are no errors.
7. Launch the **Analysis** **Running Through Environment**.
8. Set up the simulation environment as you normally would for simulating a schematic. This includes specifying a simulation file and transient run length, making the SPP file output reader in the environment options, and selecting the outputs to be plotted on the schematic.
9. In **ADD**, select **Variables** \rightarrow **Copy Properties**. The design variables **width** and **prop** should appear in the Design Variables area of the **Running Environment** window.
10. In **ADD**, select **Variables** \rightarrow **File**. In the file list, type **1** and then apply the value by clicking **Apply**. Change the value of **prop** to **0.01** and click **Apply**. Check the values have been assigned, click **OK**. See Figure 1 and 2 below.
11. In **ADD**, set the connection by selecting **Simulation** \rightarrow **Variable and Run**. After loading the file simulation is complete, the results window will pop-up. The results should be

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