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Borut Robič
Theo Ungerer

Processor
Architecture

From Dataflow to Superscalar and Beyond

Springer

Processor Architecture From Dataflow To Superscalar And Beyond

Raffaela Di Napoli

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Processor Architecture Jurij Silc,Borut Robic,Theo Ungerer,1999-06-08 A survey of architectural mechanisms and implementation techniques for exploiting fine and coarse grained parallelism within microprocessors Beginning with a review of past techniques the monograph provides a comprehensive account of state of the art techniques used in microprocessors covering both the concepts involved and implementations in sample processors The whole is rounded off with a thorough review of the research techniques that will lead to future microprocessors XXXXXXX Neuer Text This monograph surveys architectural mechanisms and implementation techniques for exploiting fine grained and coarse grained parallelism within microprocessors It presents a comprehensive account of state of the art techniques used in microprocessors that covers both the concepts involved and possible implementations The authors also provide application oriented methods and a thorough review of the research techniques that will lead to the development of future processors Parallel and Distributed Processing and Applications Minyi Guo,2003 This book constitutes the refereed proceedings of the International Symposium on Parallel and Distributed Processing and Applications ISPA 2003 held in Aizu Japan in July 2003 The 30 revised full papers and 9 revised short papers presented together with abstracts of 4 keynotes were carefully reviewed and selected from numerous submissions The papers are organized in topical sections on applications on Web based and intranet systems compiler and optimization techniques network routing performance evaluation of parallel systems wireless communication and mobile computing parallel topology data mining and evolutionary computing image processing and modeling network security and database and multimedia systems Essentials of Computer Organization and Architecture with Navigate Advantage Access Linda Null,2023-04-13 Essentials of Computer Organization and Architecture focuses on the function and design of the various components necessary to process information digitally This title presents computing systems as a series of layers taking a bottom up approach by starting with low level hardware and progressing to higher level software Its focus on real world examples and practical applications encourages students to develop a big picture understanding of how essential organization and architecture concepts are applied in the computing world In addition to direct correlation with the ACM IEEE guidelines for computer organization and architecture the text exposes readers to the inner workings of a modern digital computer through an integrated presentation of fundamental concepts and principles **Improving Processor Performance and Simulation Methodology** Joshua Jeffrey Yi,2003 **Handbook of Signal Processing Systems** Shuvra S. Bhattacharyya,Ed F. Deprettere,Rainer Leupers,Jarmo Takala,2013-06-20 Handbook of Signal Processing Systems is organized in three parts The first part motivates representative applications that drive and apply state of the art methods for design and implementation of signal processing systems the second part discusses architectures for implementing these applications the third part focuses on compilers and simulation tools describes models of computation and their associated design tools and methodologies This handbook is an essential tool for professionals in many fields and researchers of all

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