

EE6502 Microprocessors and Microcontrollers

Unit –I Part- A

1. What is Microprocessor? Give the power supply & clock frequency of 8085.

The microprocessor is a programmable device that takes in numbers, performs on them arithmetic or logical operations according to the program stored in memory and then produces other numbers as a result. It has 40 pins and uses +5V for power. It can run at a maximum frequency of 3 MHz.

2. State the use of RESET IN and RESET OUT pins of 8085 processor.

RESET IN (Input)

Reset sets the Program Counter to zero and resets the Interrupt Enable and HLDA flipflops. None of the other flags or registers (except the instruction register) are affected. The CPU is held in the reset condition as long as Reset is applied.

RESET OUT (Output)

Indicates CPU is being reset. Can be used as a system RESET. The signal is synchronized to the processor clock.

3. Mention the purpose of SID and SOD lines.

SID (Input)

Serial input data line. The data on this line is loaded into accumulator bit 7 whenever a RIM instruction is executed.

SOD (Output)

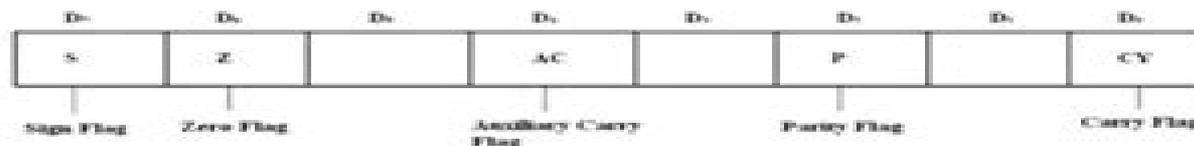
Serial output data line. The output SOD is set or reset as specified by the SIM instruction.

4. What are the functions of an accumulator?

The accumulator is an 8-bit register that is a part of arithmetic/logic unit (ALU). This register is used to store 8-bit data and to perform arithmetic and logical operations. The result of an operation is stored in the accumulator. The accumulator is also identified as register A.

5. What are the flags in 8085?

8 bit register –shows the status of the microprocessor before/after an operation–S (sign flag), Z (zero flag), AC (auxiliary carry flag), P (parity flag) & CY (carry flag)



Microprocessors Questions Answers

Barry B. Brey



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Mastering 8085 & 8086 Microprocessors: A Question-Answer Approach Author Dr. Maitrayee Chakrabarty ,Dr. Barnali kundu,Dr. Himadri Sekhar Das,Dr. Maitrayee Chakrabarty,Ms. Debodyuti Upadhaya,2026-02-14 Mastering 8085 8086 Microprocessors A Question Answer Approach is a focused textbook designed for engineering students and educators It covers the Intel 8085 8086 architectures in depth instruction sets addressing modes timing diagrams interrupts DMA and memory I O interfacing using a structured Q A format with clear diagrams and MCQs The book aligns with undergraduate syllabi boosts conceptual understanding problem solving skills and prepares readers for exams competitive assessments Written by experienced faculty it serves both classroom learning and self study Fundamentals of Microprocessors Henry O. Daley,1983 **Microprocessor Interfacing and Communication Using the Intel SDK-85** Borivoje Furht,Himanshu Parikh,1986 Microprocessors and Microcomputer Development Systems Mohamed Rafiquzzaman,1984

Microprocessor Theory and Operation J. A. Sam Wilson,Joseph Risse,1995 By covering digital circuits in addition to microprocessors and providing self tests and experiments this book makes it easy to learn microprocessor systems The text is fully integrated with circuits specifications and pinouts to be a valuable resource to both beginners and veterans

Microprocessor Handbook Joseph D. Greenfield,1985 *The Challenge of Microprocessors* M. G. Hartley,Anne Buckley,1979 *Instrumentation Technology* ,1976 *Episodes in ESP* John Swales,1985 *Surviving the Design of Microprocessor and Multimicroprocessor Systems* Veljko Milutinović,2000-08-03 This book uniquely synthesizes Professor Milutinovi s thinking on the important issues in computer architecture The result is a necessarily somewhat eclectic personal statement by one of the leaders of the field Michael J Flynn Stanford University From the Foreword How do we invest one billion transistors on a single chip asks Veljko Milutinovi as he ponders the ultimate goal of an entire distributed shared memory DSM plus numerous specialized accelerators on a single chip He then goes on to present a lively personal account complete with survival tips of his experiences in the front line of the rapidly evolving arena of microprocessor and multimicroprocessor system design Focusing on areas critical to the future of system on a chip design Milutinovi combines his unique perspective with authoritative discussions of cache instruction level parallelism prediction strategies the I O bottleneck multithreading and multiprocessors He reinforces concepts using three case studies of his own computer system accelerator implementations with additional details available through Web based appendices A key DSM concept Reflective Memory System RMS and tools for evaluating new architectural ideas or characterizing applications are also covered in appendices Designed for fast easy comprehension *Surviving the Design of Microprocessor and Multimicroprocessor Systems* integrates clear up to date explanations with a wealth of figures and a thorough review of the technical literature It brings readers up to speed with concepts essential for implementing their own ideas while addressing the most important issues facing scientists and engineers in advanced computer design **Designing Microprocessor-based Instrumentation**

Joseph J. Carr, 1982 Instrumentation Introduction to microcomputers Examination of two popular uP chips Binary arithmetic and digital codes Interfacing techniques Microprocessor support chips Solving problems with the microprocessor Address decoders Interfacing memory Interfacing I O ports Signals and noise Operational amplifiers Transducers Sample Hold circuits Analog reference circuits Interfacing keyboards switches and displays Basics of data conversion D A Basics of data conversion A D Data acquisition systems Data converter interfacing Software data conversion Z80 instructions sorted by Op code Z80 instructions sorted by mnemonic Z80 8080 instruction equivalence

Microprocessor Software Project Management Eli T. Fathi, Cedric V. W. Armstrong, Ontario Centre for Microelectronics, 1985 *Microcomputers and Microprocessors* John E. Uffenbeck, 1991 An introduction to microprocessors updated to cover recent models Designed as a first course in microcomputers this new edition covers the hardware and machine language software of the 8080 8085 and Z 80 8 bit microprocessors It explores various aspects of microcomputer technology using examples of 8080 8085 and Z 80 applications *Microprocessors for Management, CAD, CAM, and Robotics* Dimitris N. Chorafas, 1982 The challenge of continuing development New horizons Applying microprocessor technology Device architecture Product incentives An information policy Designing manufacturing and interfacing The wafer Quality assurance Engineering premises Interconnecting and interfacing Firmware and microcode Computer aided design A CAD methodology Robotics Tuning the manufacturing capability A process in control

Digital Electronics William Kleitz, 2002 For freshman sophomore undergraduate level courses in Digital Electronics This easy to understand book illustrates practical applications using circuits the student will face on the job *Bulletin of the Institution of Engineers (India)*. Institution of Engineers (India), 1984 *The Intel Microprocessors* Barry B. Brey, 2006 KEY BENEFIT Updated and current this book provides a comprehensive view of programming and interfacing of the Intel family of microprocessors from the 8088 through the latest Pentium 4 microprocessor KEY TOPICS Organized in an orderly and manageable format it offers over 200 programming examples using the Microsoft Macro Assembler program and provides a thorough description of each Intel family members memory systems and various I O systems MARKET For Electronic engineering specialist programmers computer scientists or electrical engineers

The Motorola Microprocessor Family Barry B. Brey, 1992 **A Verified Compiler for a Verified Microprocessor** Jeffrey J. Joyce, 1989 **California. Court of Appeal (2nd Appellate District). Records and Briefs** California (State),

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