| Memory | | CPU | | | I/O Bus | I/O Interface | Device |

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                    ┌─────────────────────────────┐
                    │           CPU               │
                    │  ┌─────┐ ┌─────┐ ┌─────┐     │
                    │  │ ALU │ │ PC  │ │ IR  │     │
                    │  └─────┘ └─────┘ └─────┘     │
┌────────┐          │                             │   ┌──────┐   ┌───────────┐   ┌────────┐
│        │◄─Address─┤  ┌─────┐                    ├──►│      │──►│    I/O    │──►│        │
│        │          │  │ MAR │      Bus           │   │      │   │ Interface │   │ Device │
│        │          │  └─────┘                    │   │      │   └───────────┘   └────────┘
│        │◄─ Data ──┤  ┌─────┐      Bus           │   │ I/O  │
│ Memory │          │  │ MDR │◄─────────────      │   │ Bus  │
│        │─────────►│  └─────┘                    │   │      │
│        │          │                             │   │      │   ┌───────────┐   ┌────────┐
│        │◄─Control─┤  ┌─────┐      Bus           ├──►│      │──►│    I/O    │──►│        │
│        │          │  │ CU  │                    │   │      │   │ Interface │   │ Device │
└────────┘          │  └─────┘                    │   └──────┘   └───────────┘   └────────┘
                    │  ┌────────────┐ ┌──────────┐│
                    │  │Accumulator │ │ General  ││
                    │  │            │ │ Purpose  ││
                    │  │            │ │ Register ││
                    │  └────────────┘ └──────────┘│
                    │           CPU               │
                    └─────────────────────────────┘
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Memory

ALU PC IR

Address MAR Bus

Data MDR Bus

Control CU Bus

Accumulator General Purpose Register

CPU

I/O Bus

I/O Interface Device

I/O Interface Device

Computer Organization Design Interface Architecture

David A. Patterson,John L. Hennessy

Computer Organization Design Interface Architecture:

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