

**RESET**

$\overline{\text{MCLR/VPP}}$  → 1

**PORTA**

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$\overline{\text{RA1/AN1}}$  ↔ 3

$\overline{\text{RA2/AN2/VREF-/CVREF}}$  ↔ 4

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**PIC16F877A**

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39 ↔  $\overline{\text{RB6/PGC}}$

38 ↔  $\overline{\text{RB5}}$

37 ↔  $\overline{\text{RB4}}$

36 ↔  $\overline{\text{RB3/PGM}}$

35 ↔  $\overline{\text{RB2}}$

34 ↔  $\overline{\text{RB1}}$

33 ↔  $\overline{\text{RB0/INT}}$

**PORTB**

32 ←  $\text{VDD}$

31 ←  $\text{VSS}$

**POWER**

30 ↔  $\overline{\text{RD7/PSP7}}$

29 ↔  $\overline{\text{RD6/PSP6}}$

28 ↔  $\overline{\text{RD5/PSP5}}$

27 ↔  $\overline{\text{RD4/PSP4}}$

**PORTD**

26 ↔  $\overline{\text{RC7/RX/Di}}$

25 ↔  $\overline{\text{RC6/TX/CK}}$

24 ↔  $\overline{\text{RC5/SDO}}$

23 ↔  $\overline{\text{RC4/SDI/SDA}}$

**PORTC**

22 ↔  $\overline{\text{RD3/PSP3}}$

21 ↔  $\overline{\text{RD2/PSP2}}$

# Pic Microcontroller 16f877a Pin Diagram Explanation

**Jiyuan Zhang**



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